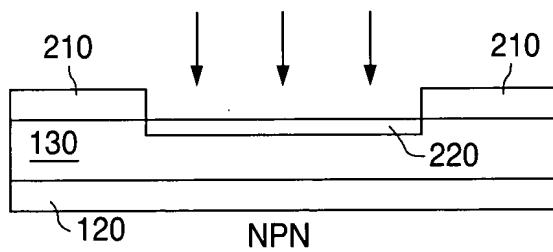
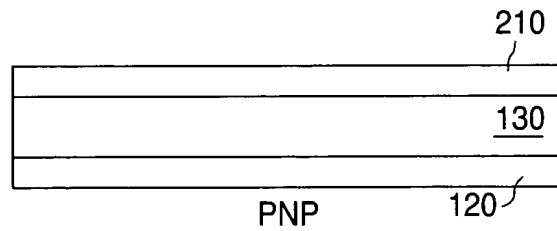


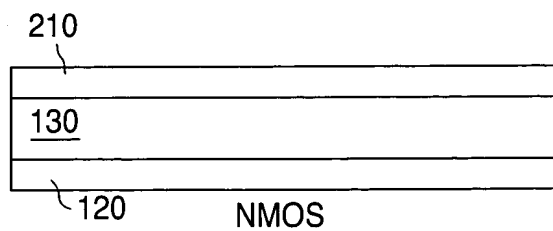
**FIG. 1**



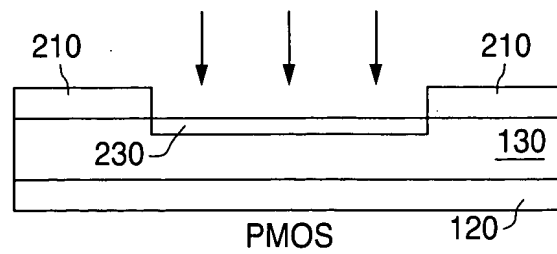
**FIG. 2A**



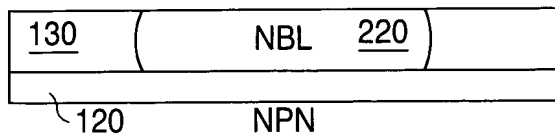
**FIG. 2B**



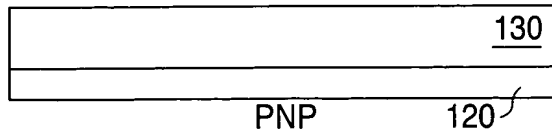
**FIG. 2C**



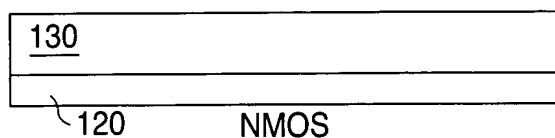
**FIG. 2D**



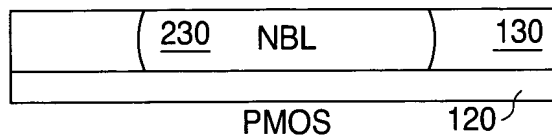
**FIG. 3A**



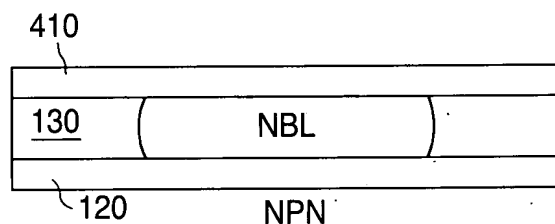
**FIG. 3B**



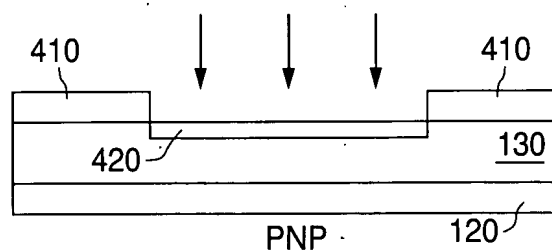
**FIG. 3C**



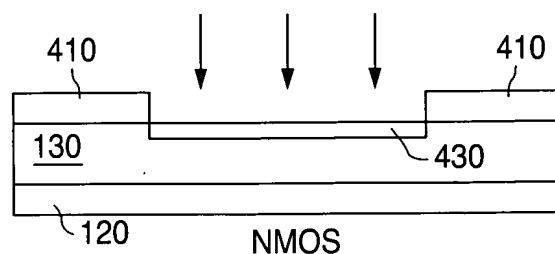
**FIG. 3D**



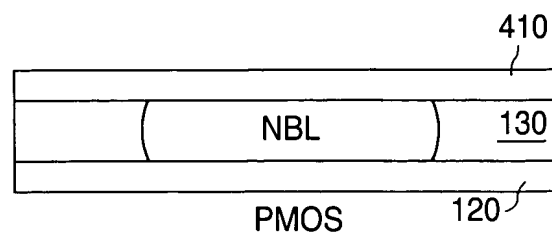
**FIG. 4A**



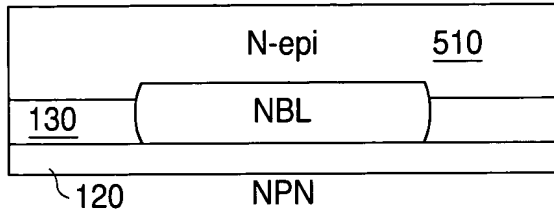
**FIG. 4B**



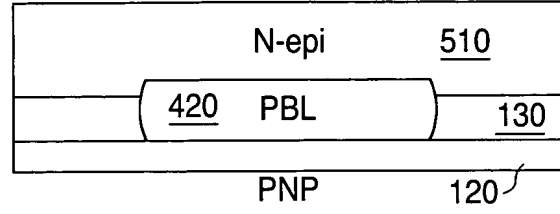
**FIG. 4C**



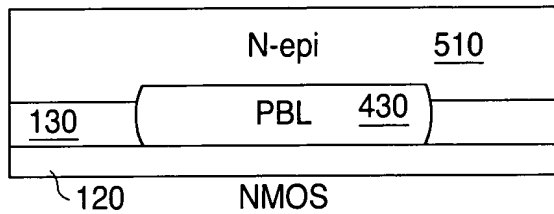
**FIG. 4D**



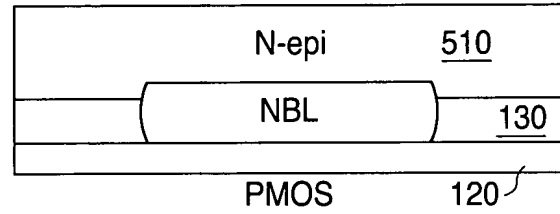
**FIG. 5A**



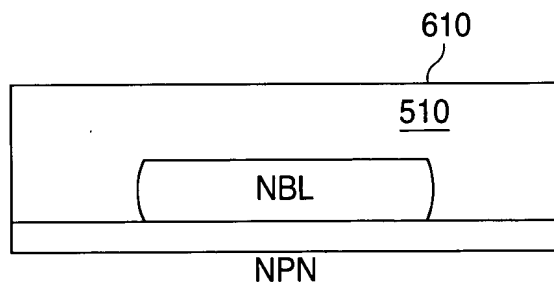
**FIG. 5B**



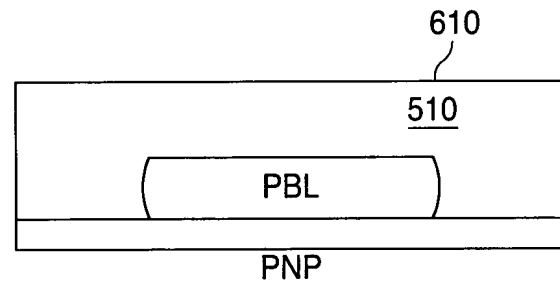
**FIG. 5C**



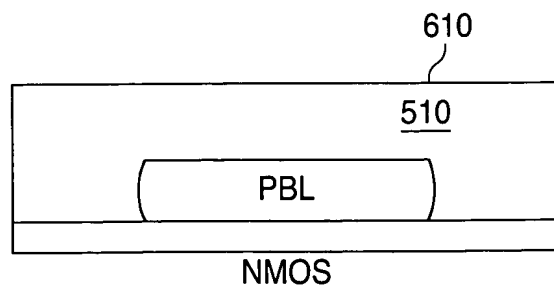
**FIG. 5D**



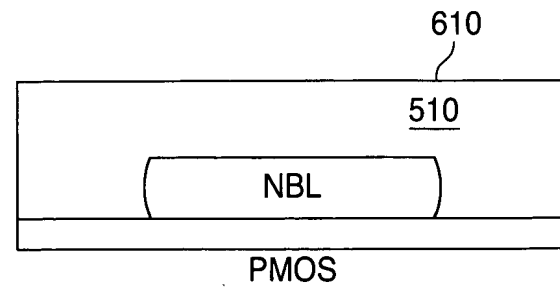
**FIG. 6A**



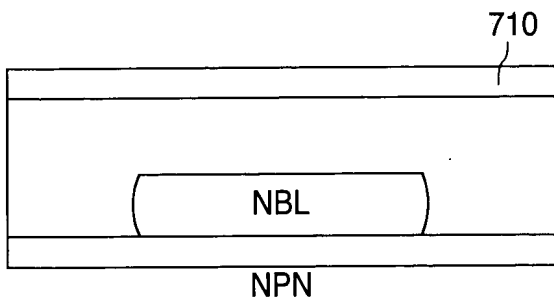
**FIG. 6B**



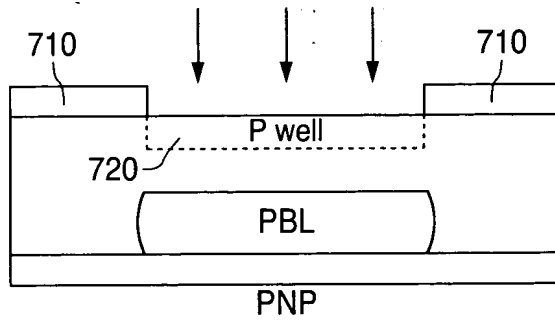
**FIG. 6C**



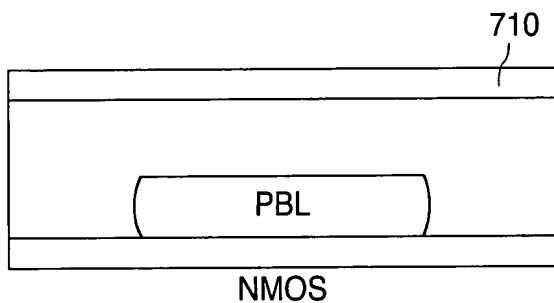
**FIG. 6D**



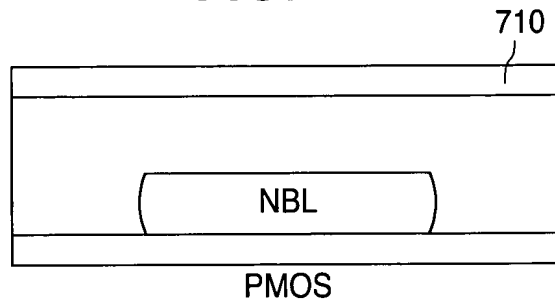
NPN  
**FIG. 7A**



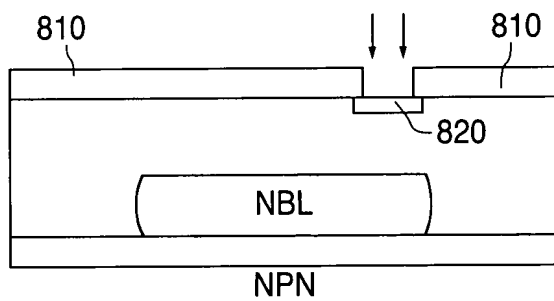
PNP  
**FIG. 7B**



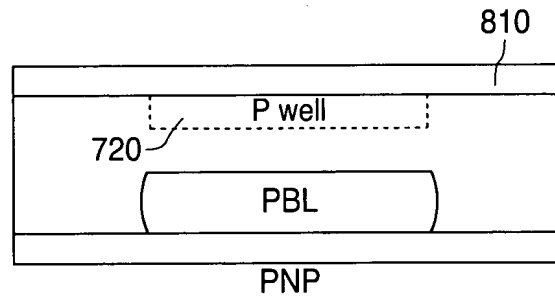
NMOS  
**FIG. 7C**



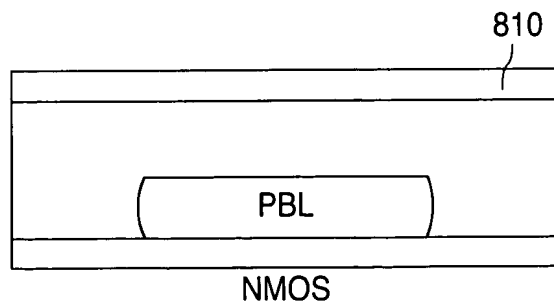
PMOS  
**FIG. 7D**



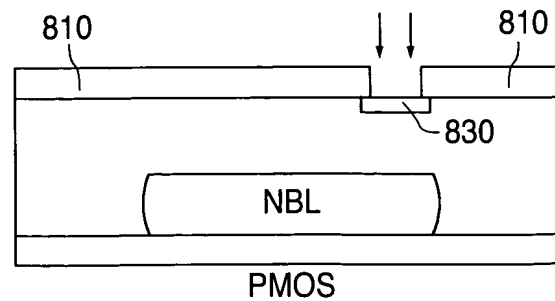
NPN  
**FIG. 8A**



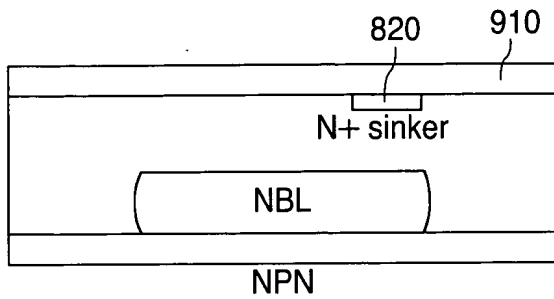
PNP  
**FIG. 8B**



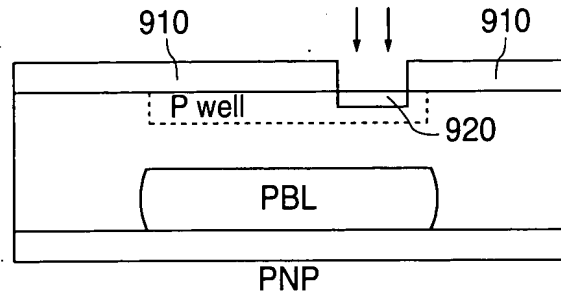
NMOS  
**FIG. 8C**



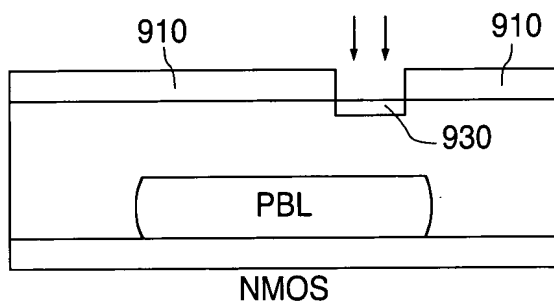
PMOS  
**FIG. 8D**



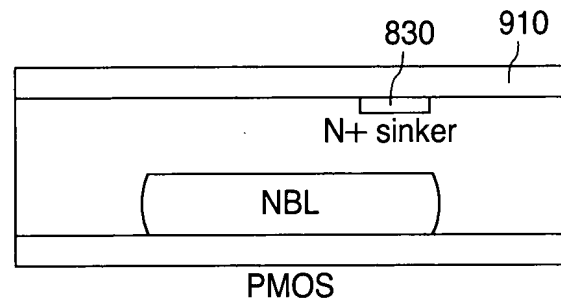
**FIG. 9A**



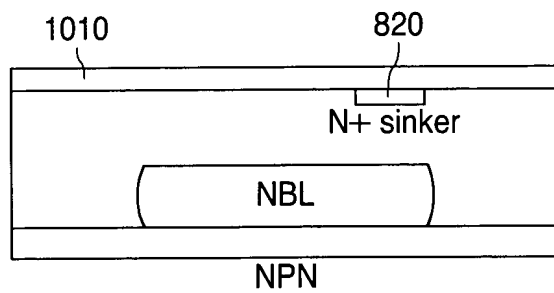
**FIG. 9B**



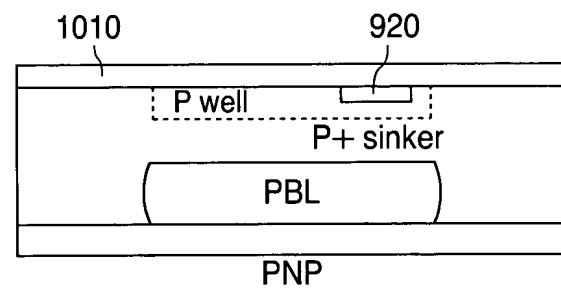
**FIG. 9C**



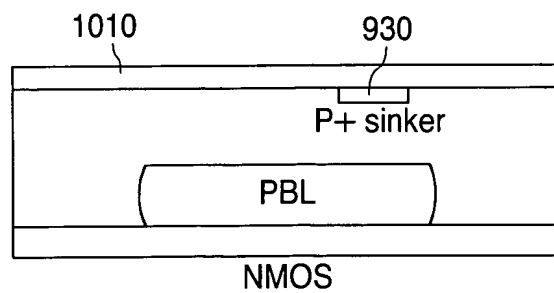
**FIG. 9D**



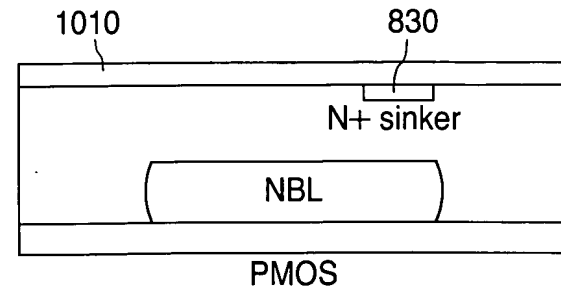
**FIG. 10A**



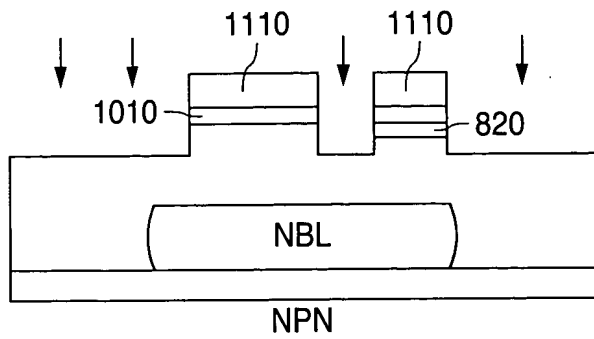
**FIG. 10B**



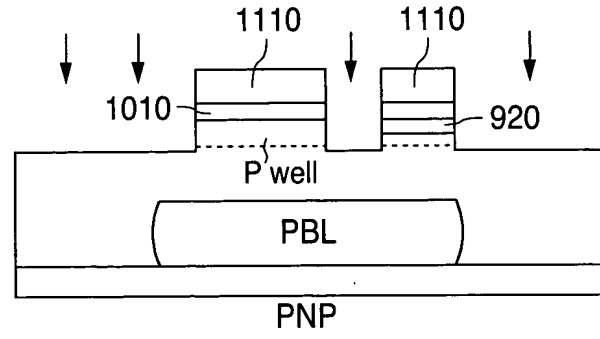
**FIG. 10C**



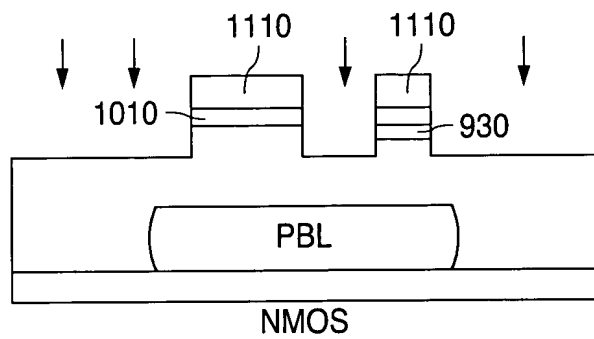
**FIG. 10D**



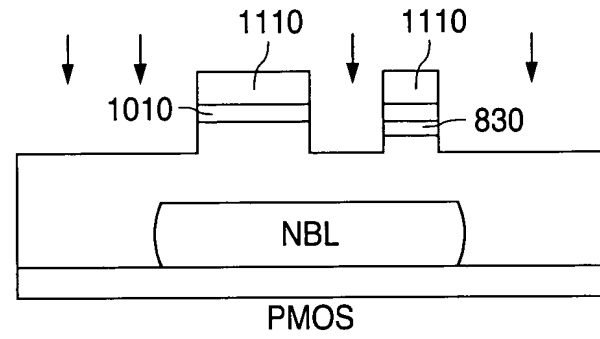
**FIG. 11A**



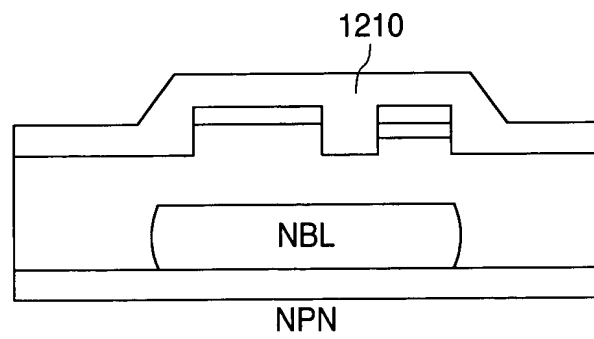
**FIG. 11B**



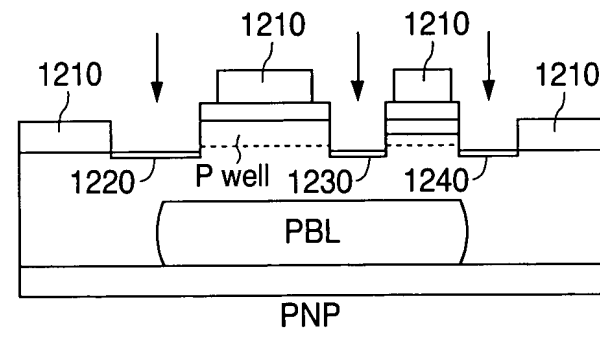
**FIG. 11C**



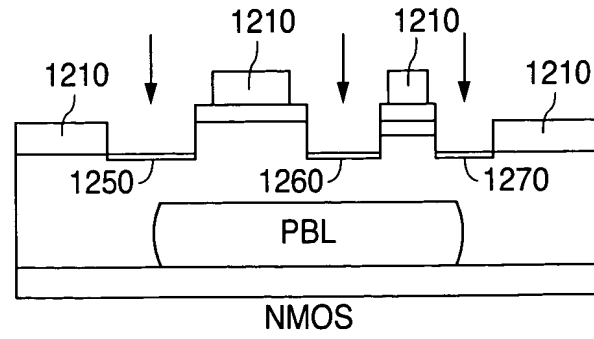
**FIG. 11D**



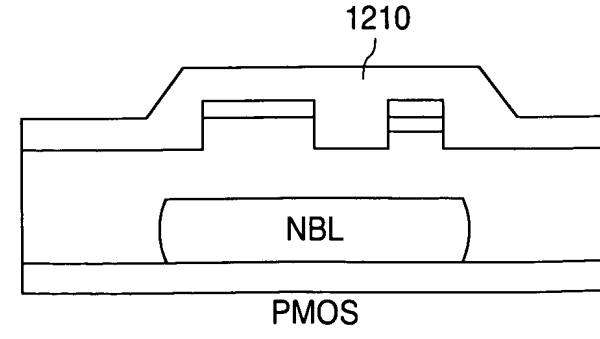
**FIG. 12A**



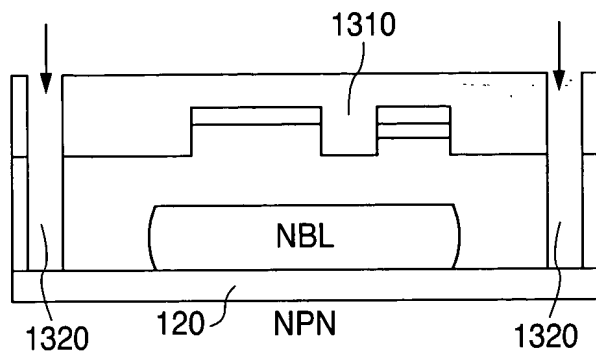
**FIG. 12B**



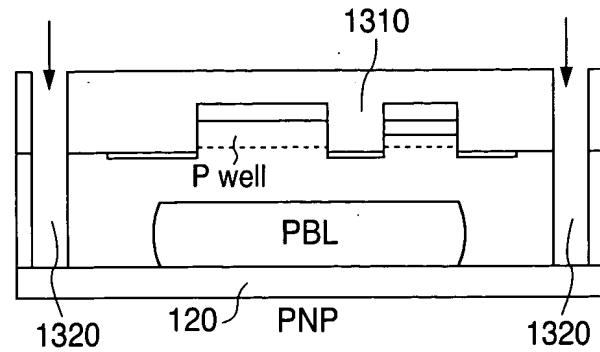
**FIG. 12C**



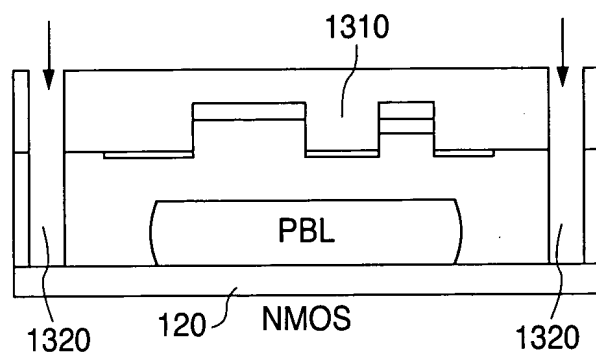
**FIG. 12D**



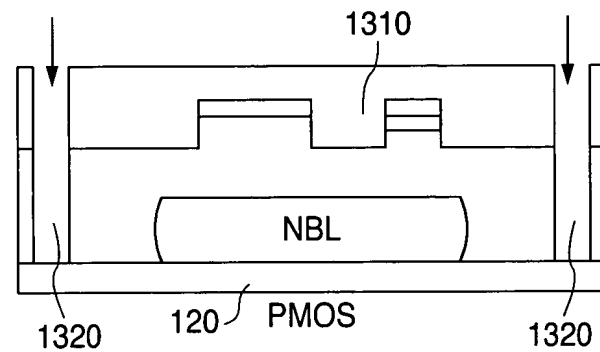
**FIG. 13A**



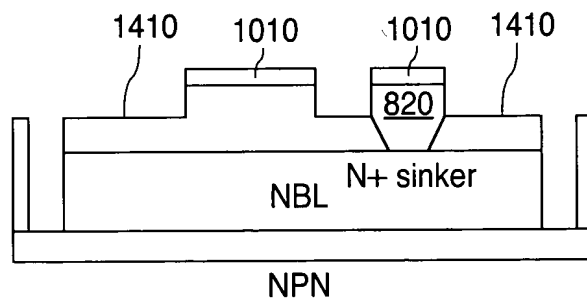
**FIG. 13B**



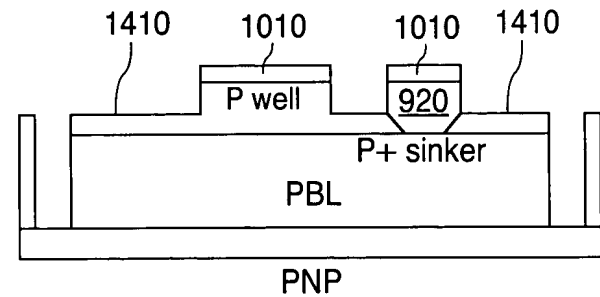
**FIG. 13C**



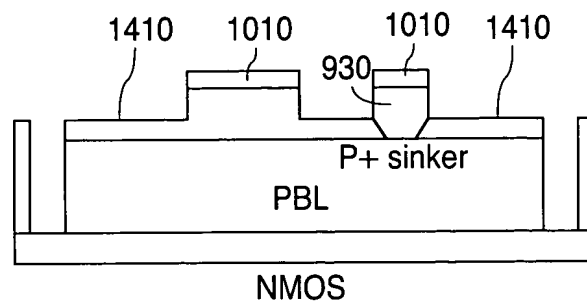
**FIG. 13D**



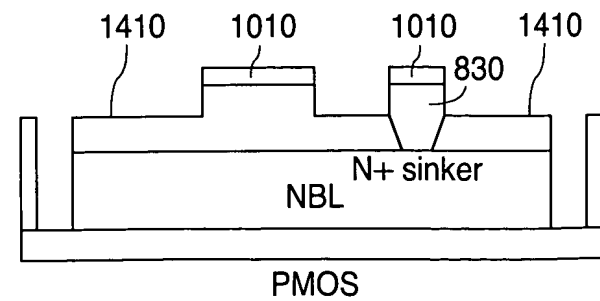
**FIG. 14A**



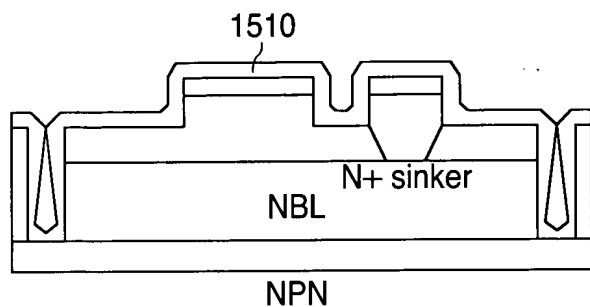
**FIG. 14B**



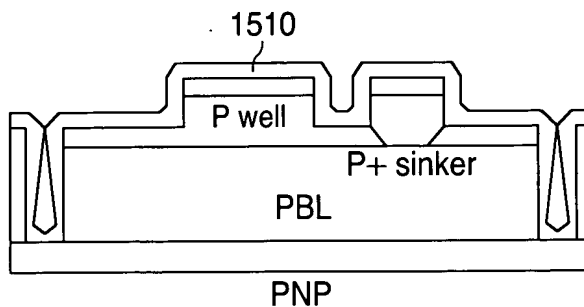
**FIG. 14C**



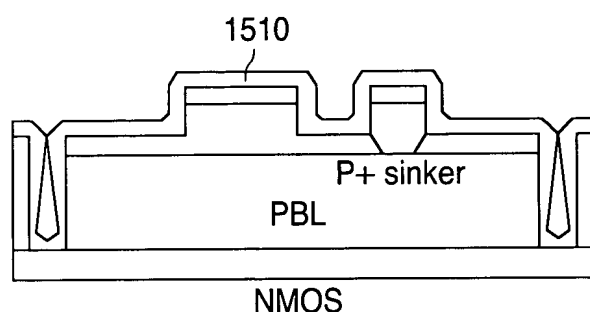
**FIG. 14D**



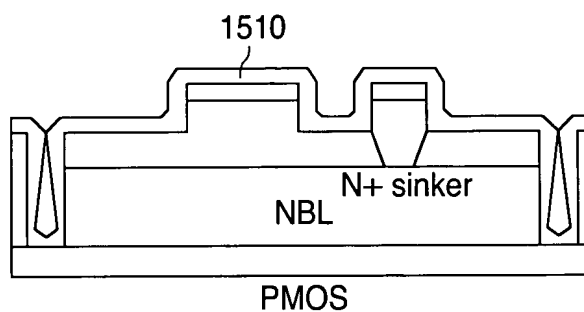
**FIG. 15A**



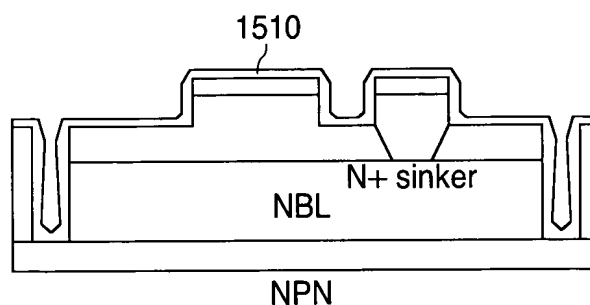
**FIG. 15B**



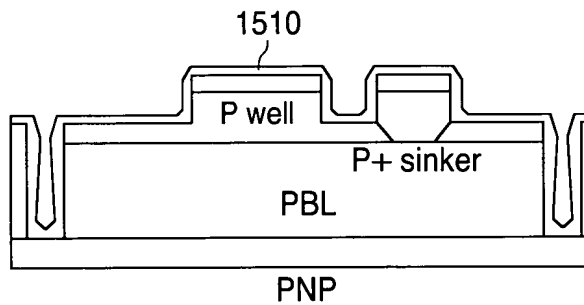
**FIG. 15C**



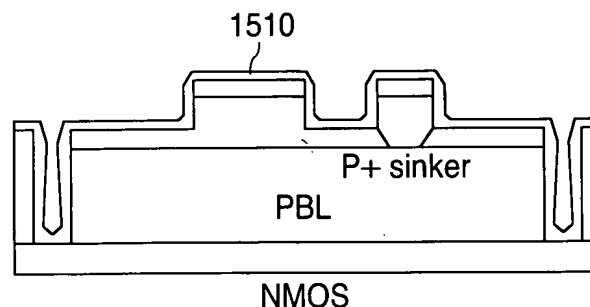
**FIG. 15D**



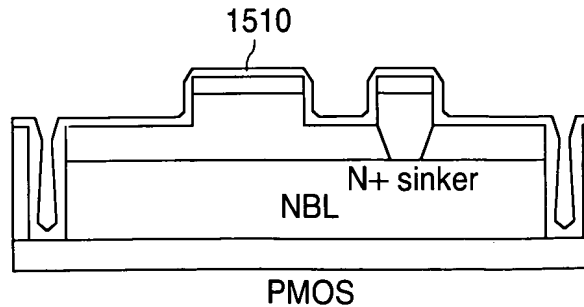
**FIG. 16A**



**FIG. 16B**

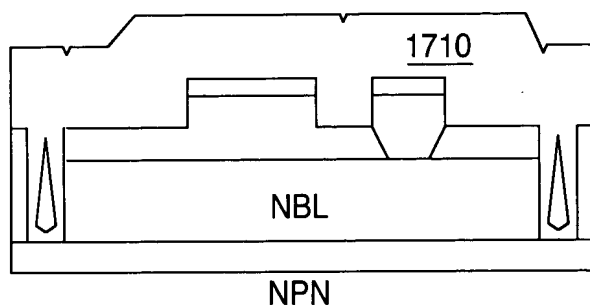


**FIG. 16C**

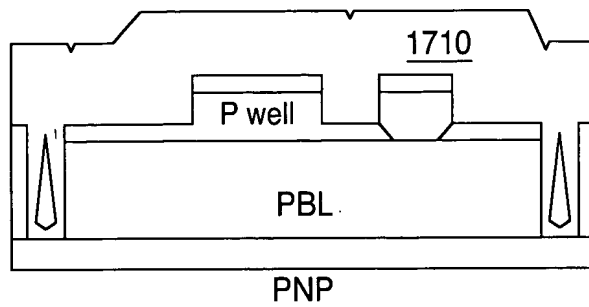


**FIG. 16D**

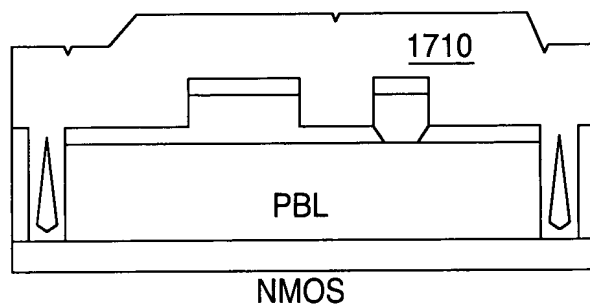




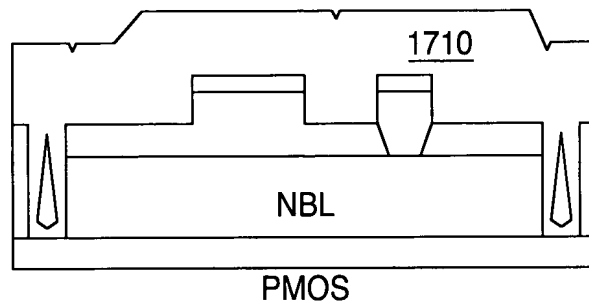
NPN  
**FIG. 17A**



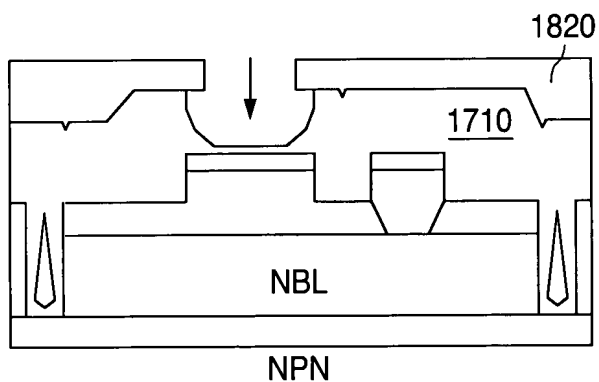
PNP  
**FIG. 17B**



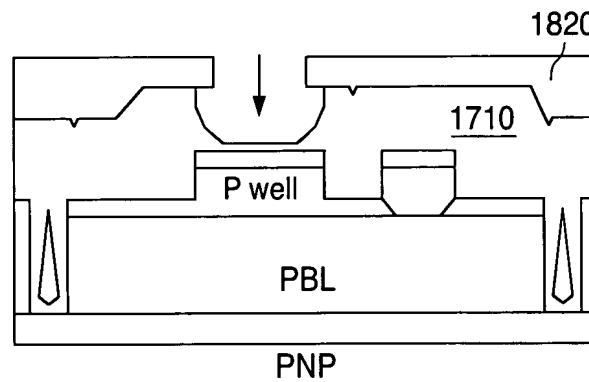
NMOS  
**FIG. 17C**



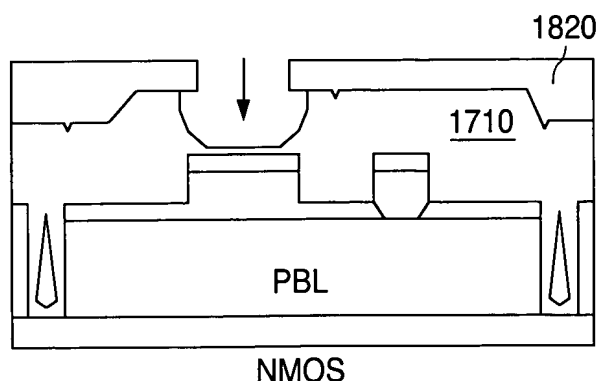
PMOS  
**FIG. 17D**



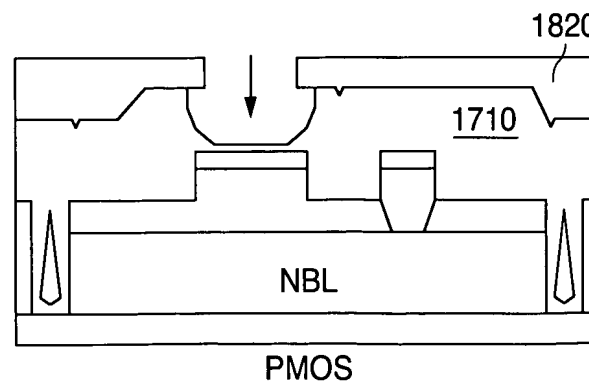
NPN  
**FIG. 18A**



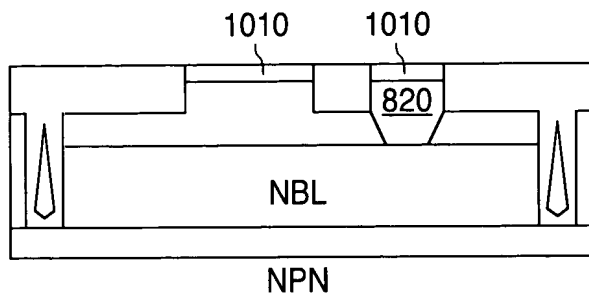
PNP  
**FIG. 18B**



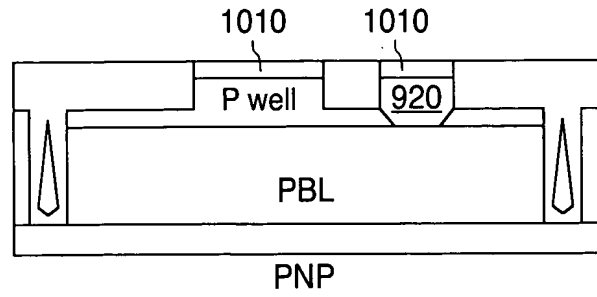
NMOS  
**FIG. 18C**



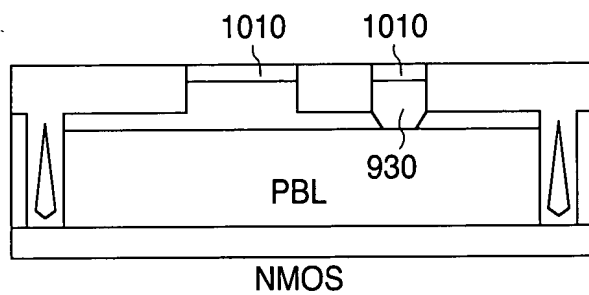
PMOS  
**FIG. 18D**



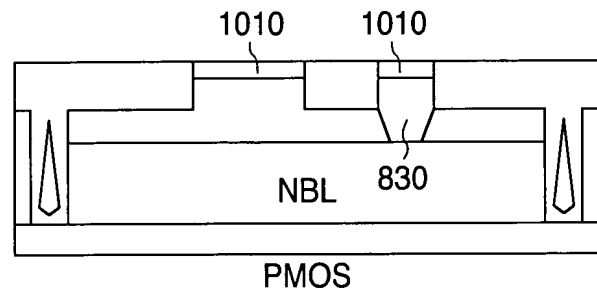
**FIG. 19A**



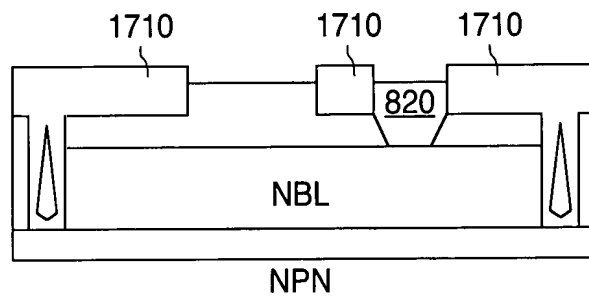
**FIG. 19B**



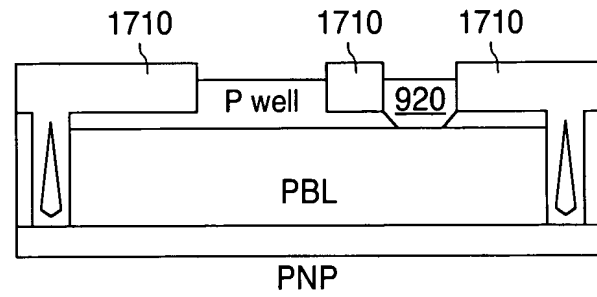
**FIG. 19C**



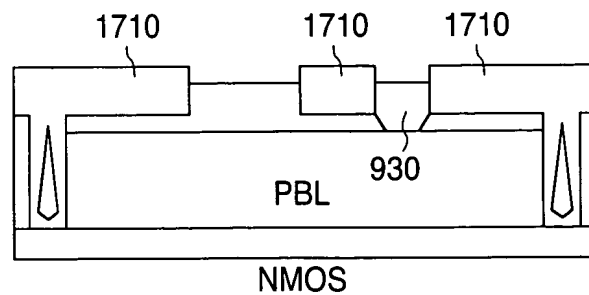
**FIG. 19D**



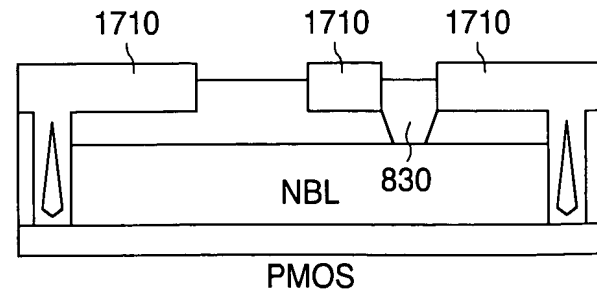
**FIG. 20A**



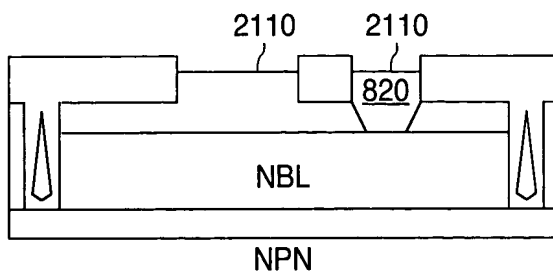
**FIG. 20B**



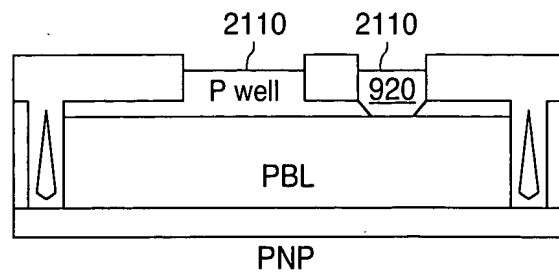
**FIG. 20C**



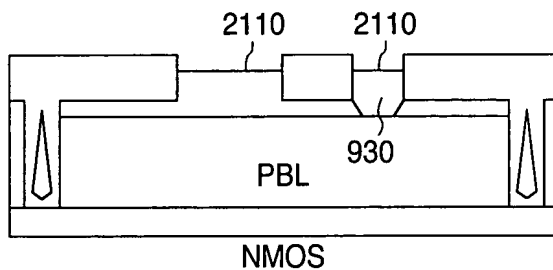
**FIG. 20D**



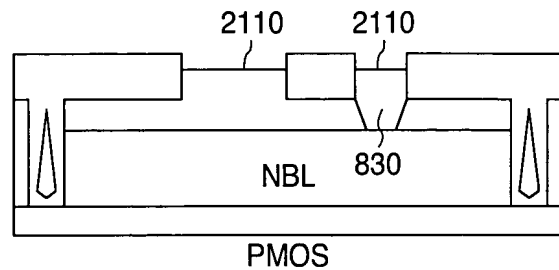
**FIG. 21A**



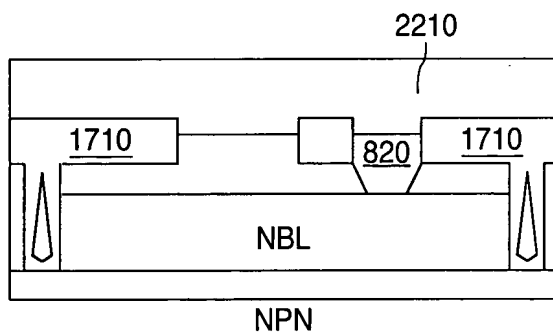
**FIG. 21B**



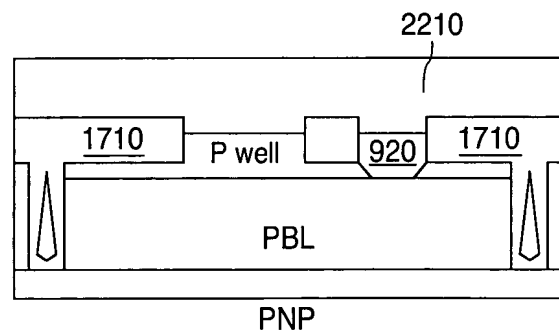
**FIG. 21C**



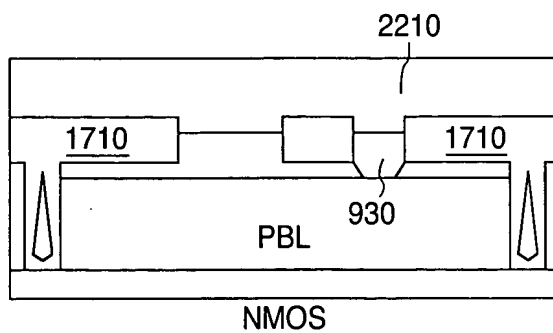
**FIG. 21D**



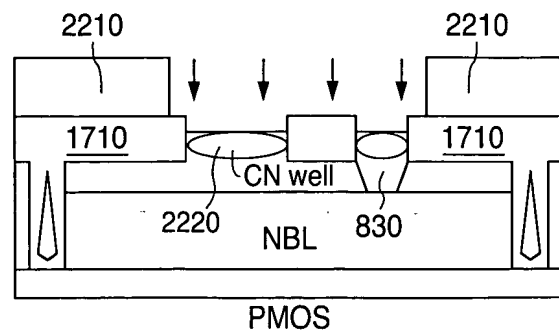
**FIG. 22A**



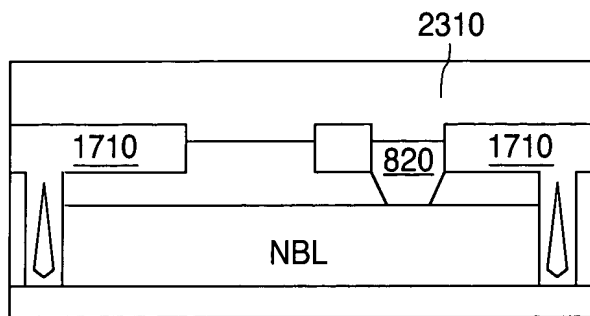
**FIG. 22B**



**FIG. 22C**

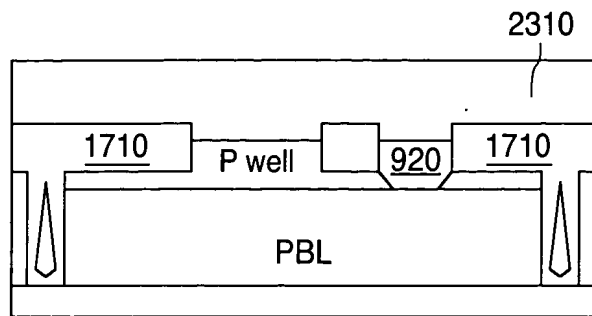


**FIG. 22D**



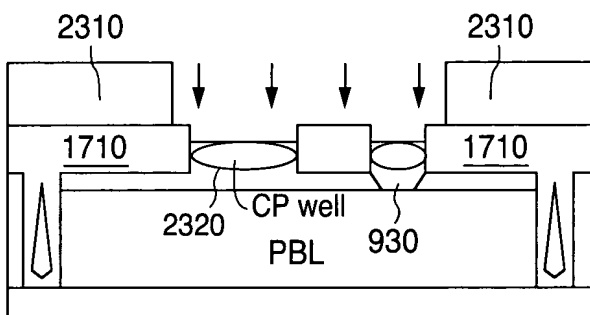
NPN

**FIG. 23A**



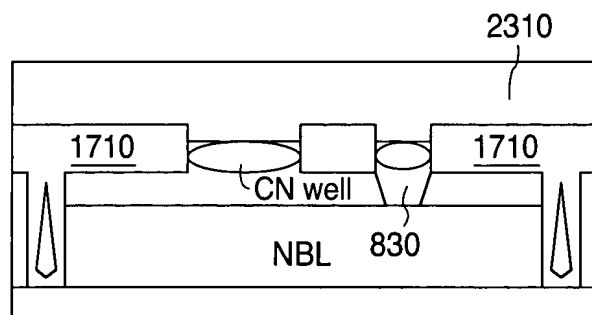
PNP

**FIG. 23B**



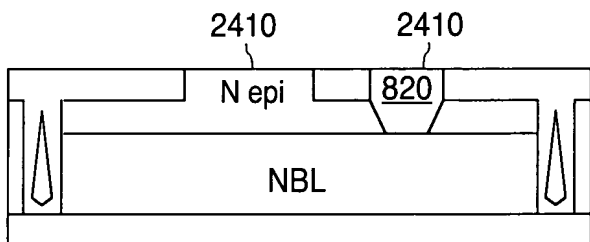
NMOS

**FIG. 23C**



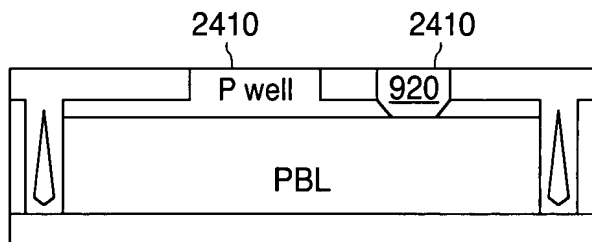
PMOS

**FIG. 23D**



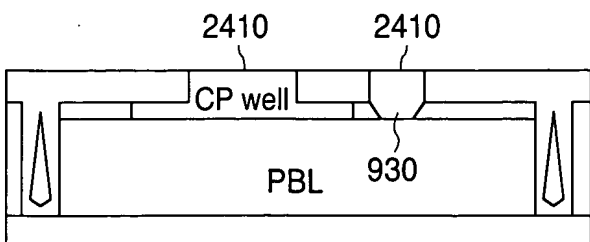
NPN

**FIG. 24A**



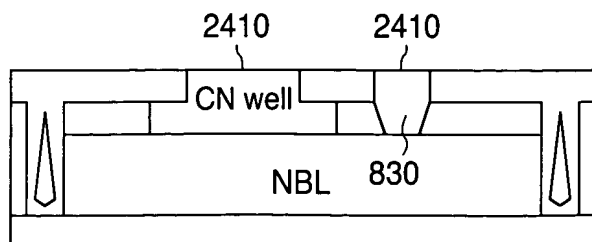
PNP

**FIG. 24B**



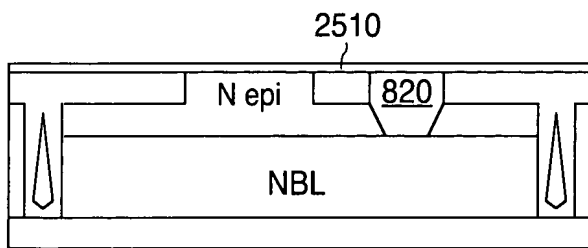
NMOS

**FIG. 24C**

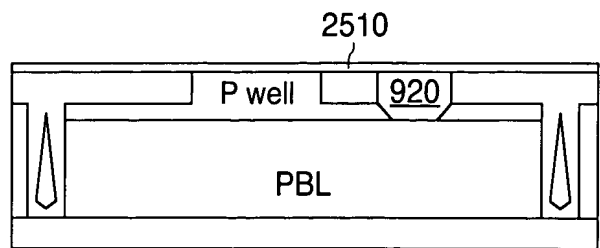


PMOS

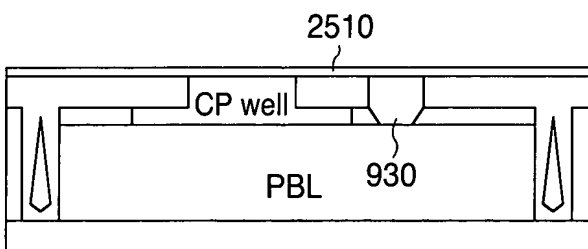
**FIG. 24D**



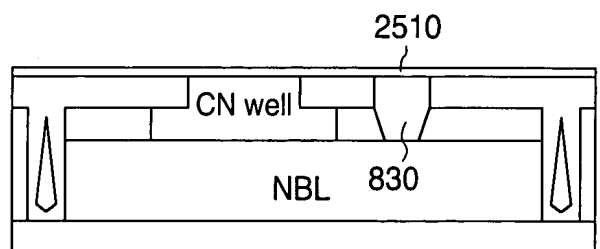
NPN  
**FIG. 25A**



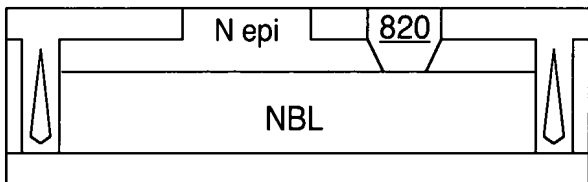
PNP  
**FIG. 25B**



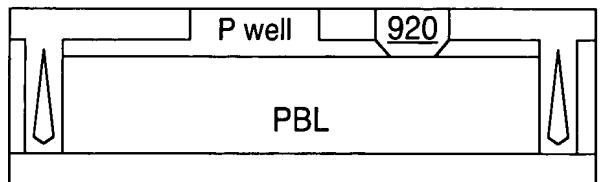
NMOS  
**FIG. 25C**



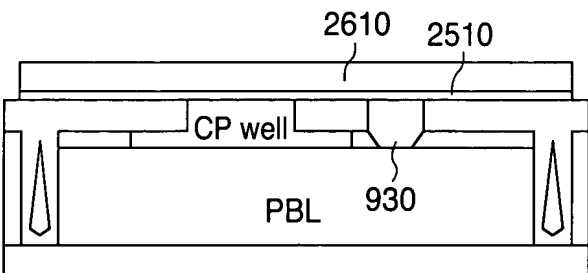
PMOS  
**FIG. 25D**



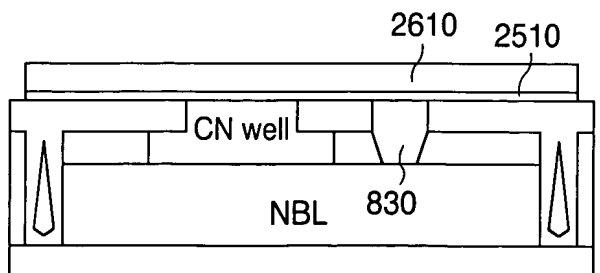
NPN  
**FIG. 26A**



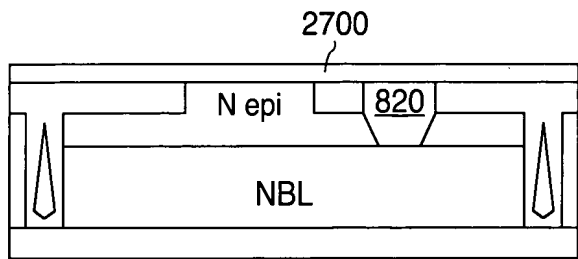
PNP  
**FIG. 26B**



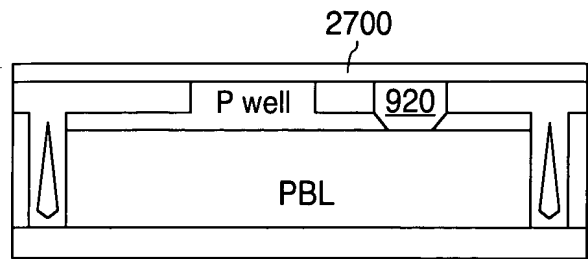
NMOS  
**FIG. 26C**



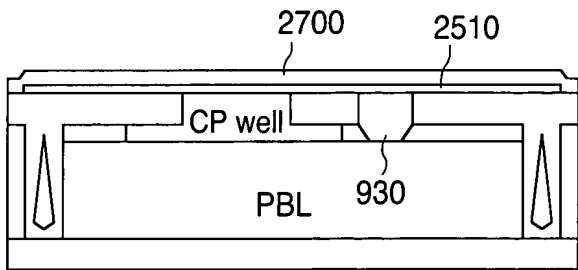
PMOS  
**FIG. 26D**



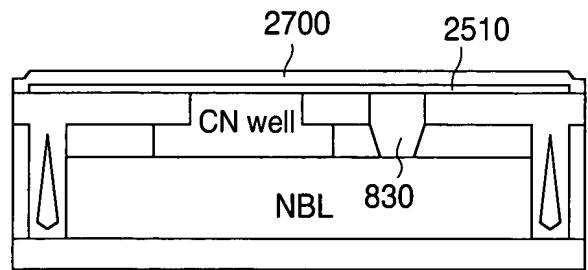
NPN  
**FIG. 27A**



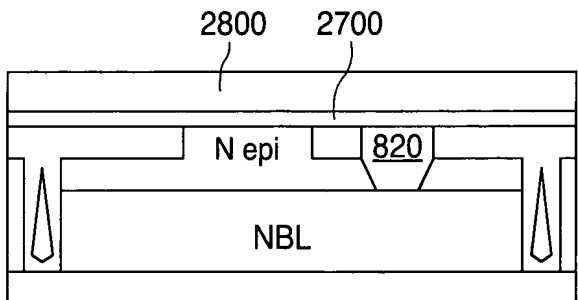
PNP  
**FIG. 27B**



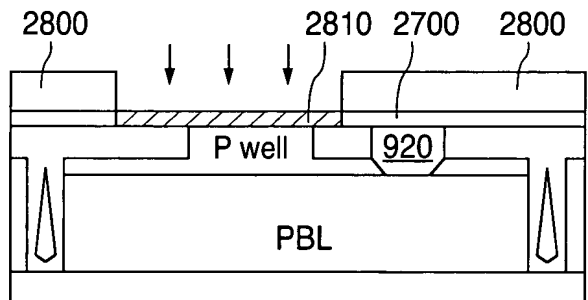
NMOS  
**FIG. 27C**



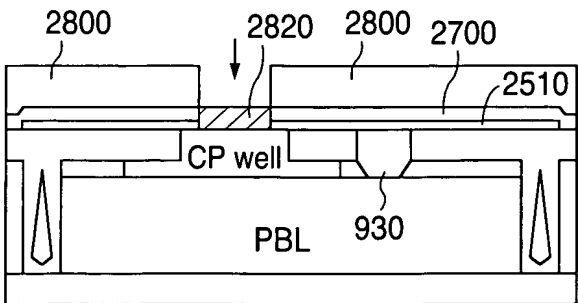
PMOS  
**FIG. 27D**



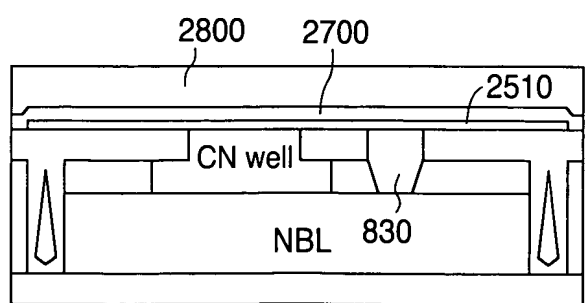
NPN  
**FIG. 28A**



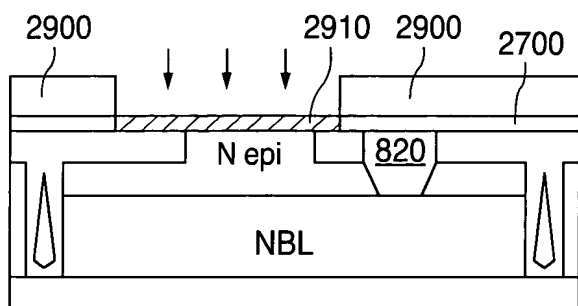
PNP  
**FIG. 28B**



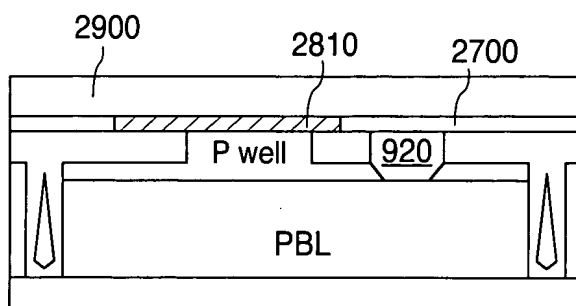
NMOS  
**FIG. 28C**



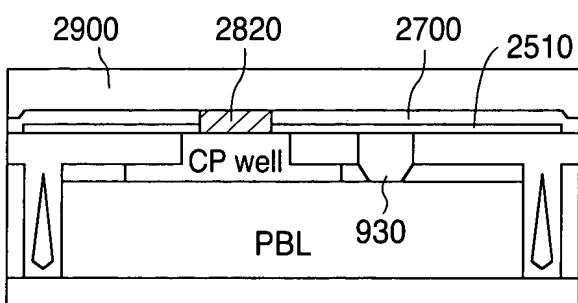
PMOS  
**FIG. 28D**



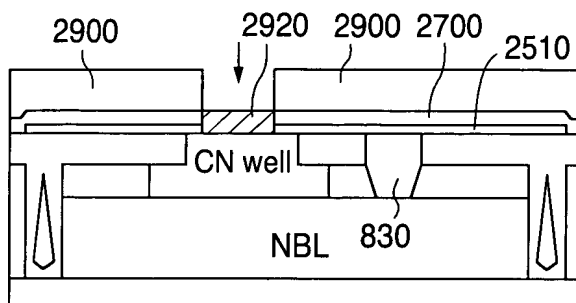
NPN  
**FIG. 29A**



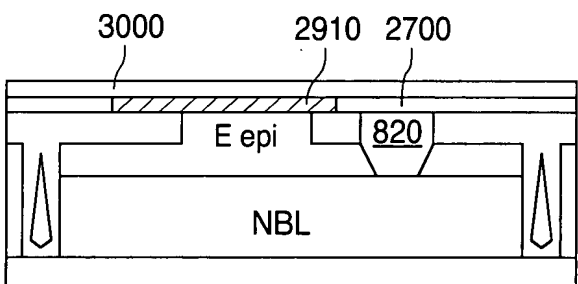
PNP  
**FIG. 29B**



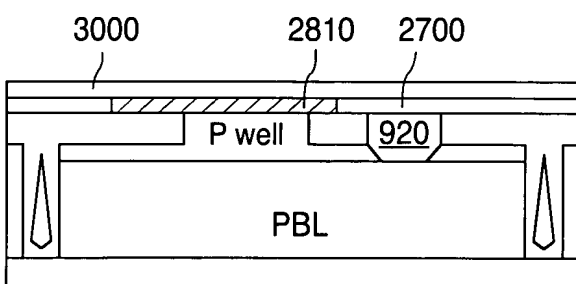
NMOS  
**FIG. 29C**



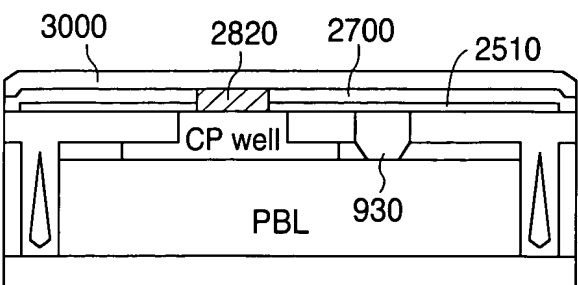
PMOS  
**FIG. 29D**



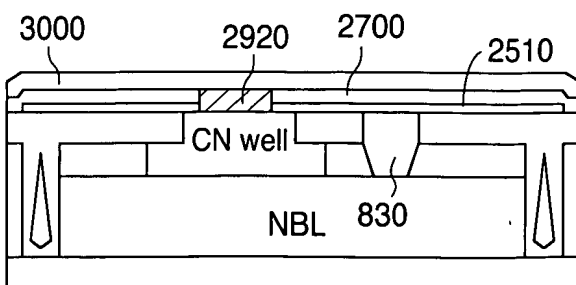
NPN  
**FIG. 30A**



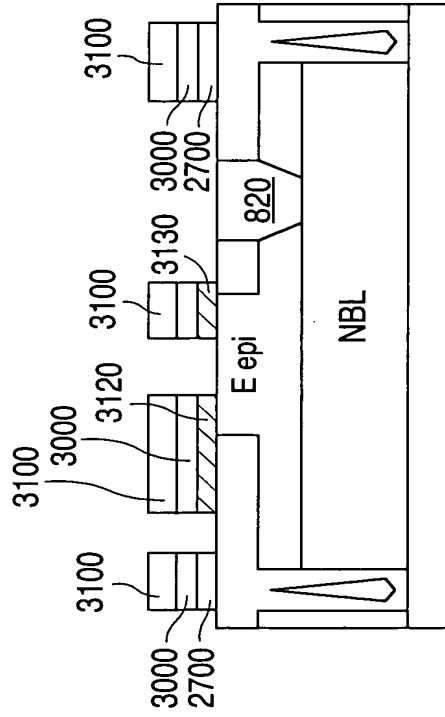
PNP  
**FIG. 30B**



NMOS  
**FIG. 30C**

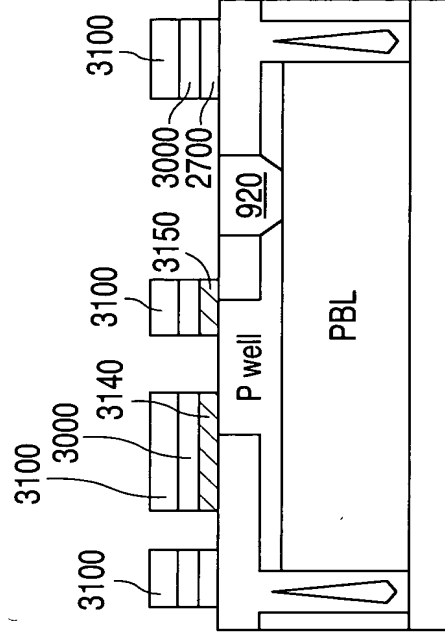


PMOS  
**FIG. 30D**



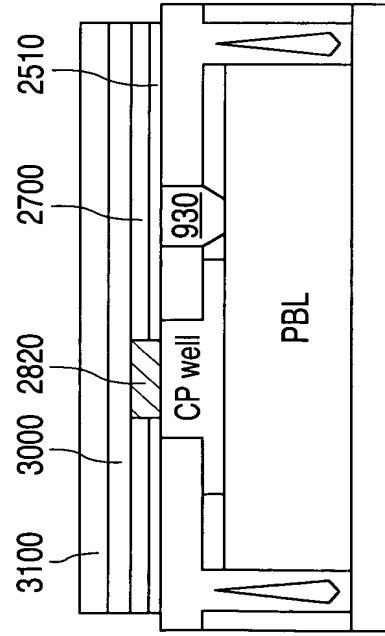
NPN

**FIG. 31A**



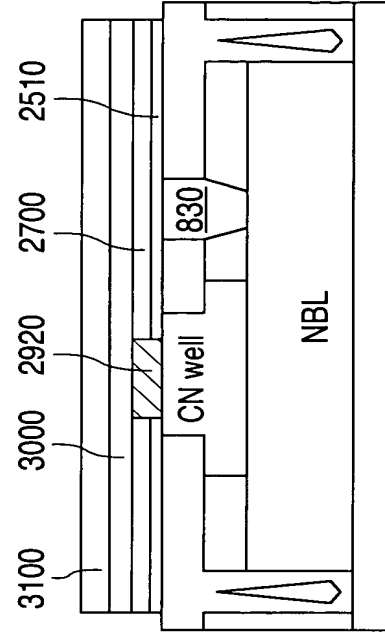
PNP

**FIG. 31B**



NMOS

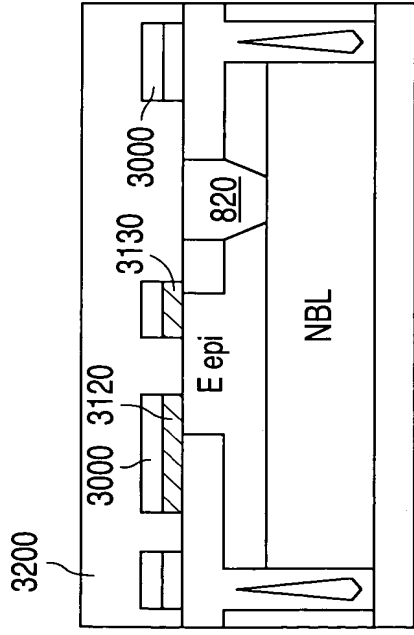
**FIG. 31C**



PMOS

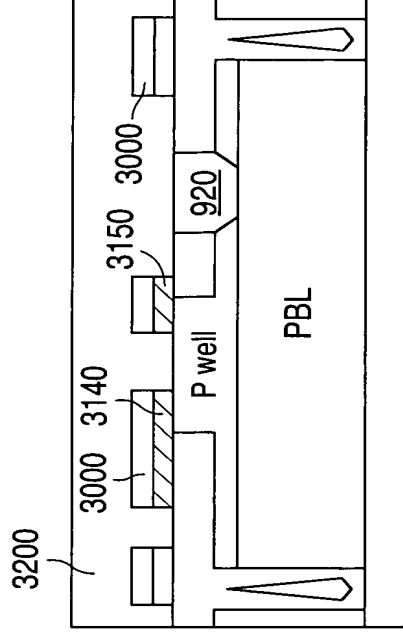
**FIG. 31D**





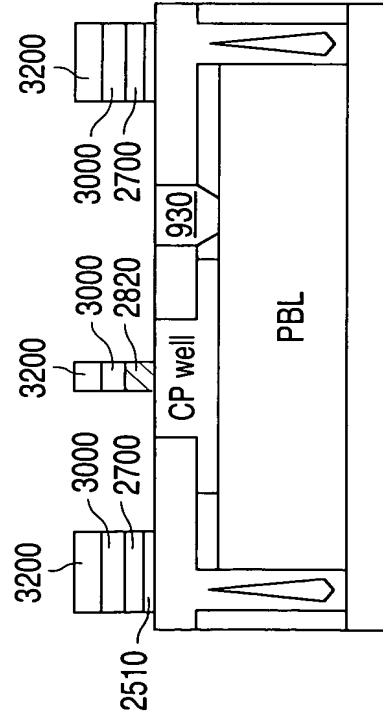
NPN

**FIG. 32A**



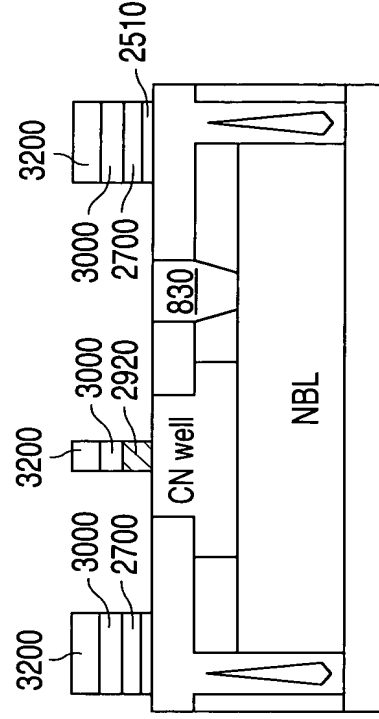
PNP

**FIG. 32B**



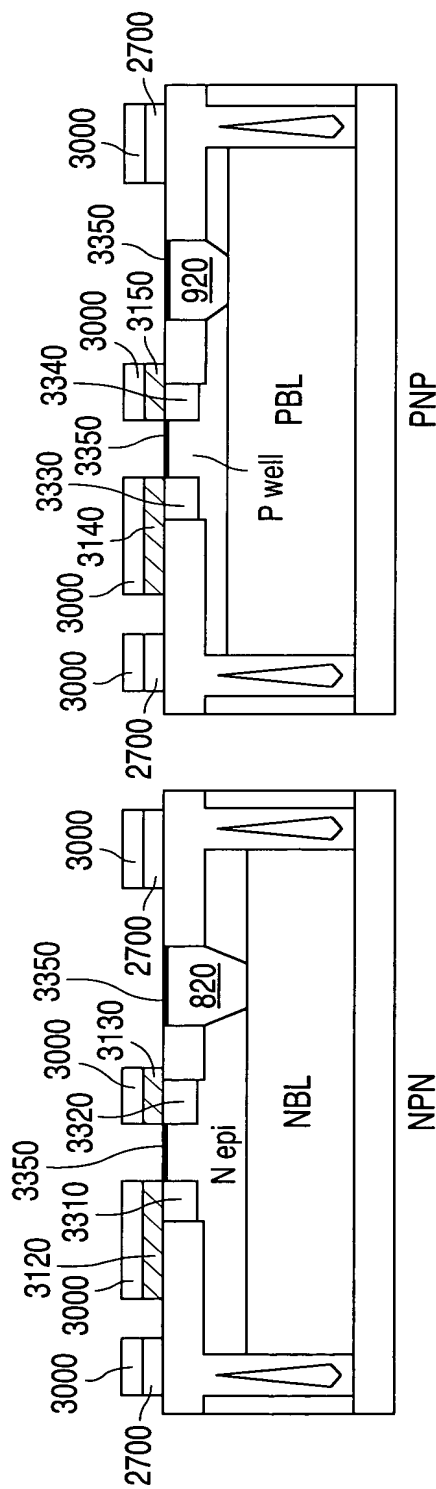
NMOS

**FIG. 32C**



PMOS

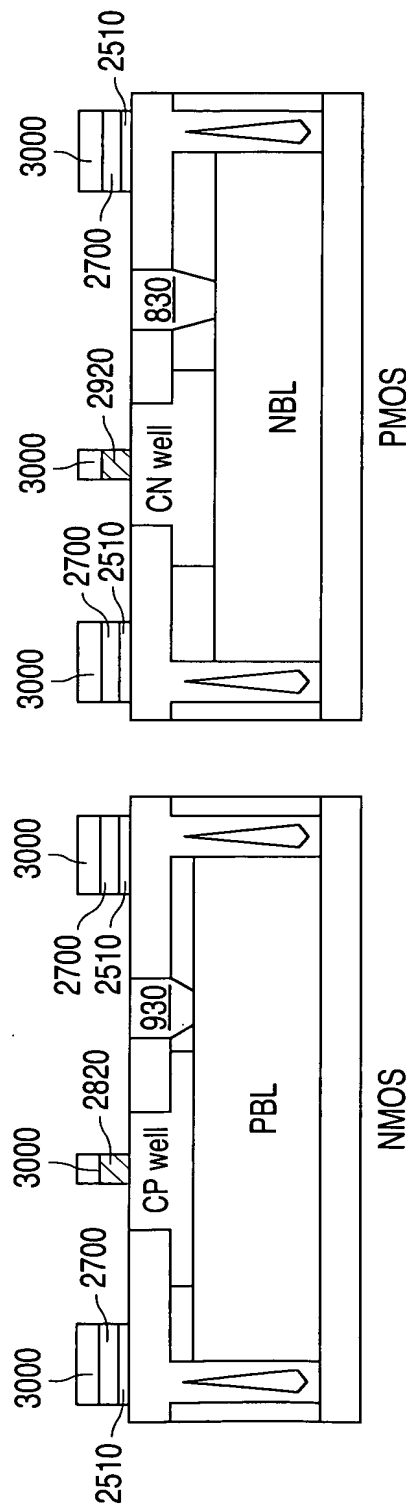
**FIG. 32D**



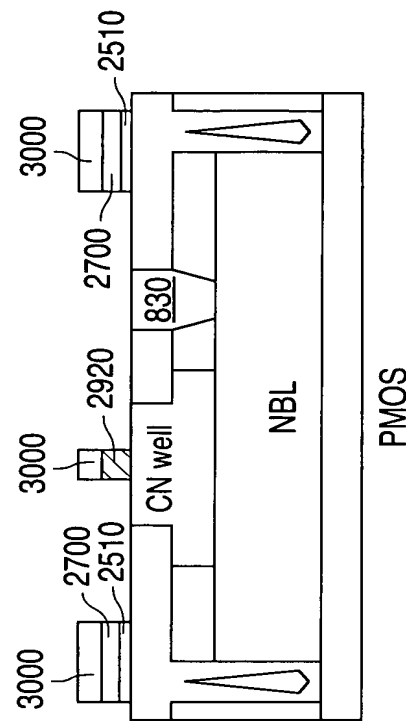
**FIG. 33A**



**FIG. 33B**



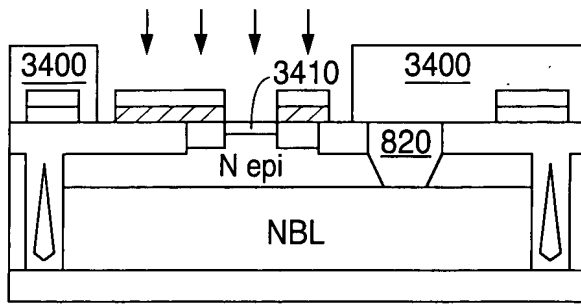
**FIG. 33C**



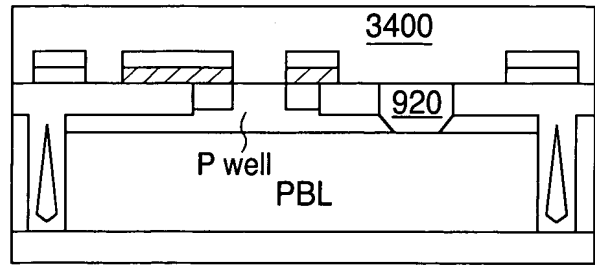
**FIG. 33D**



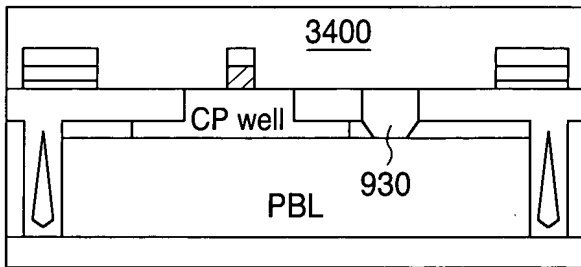
## NMOS



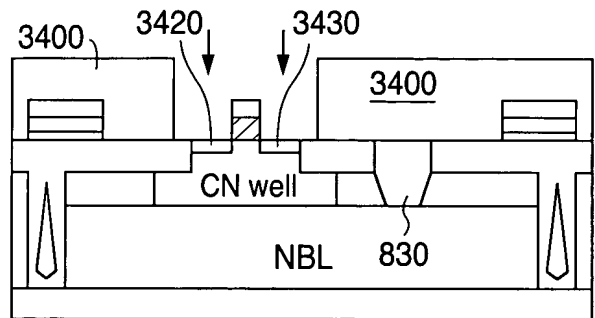
NPN  
**FIG. 34A**



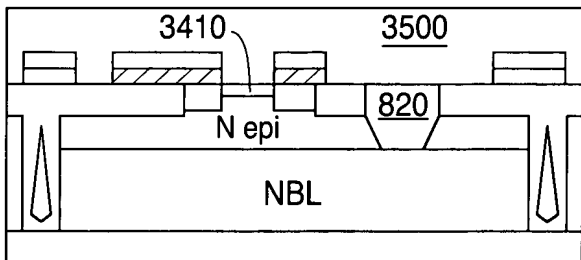
PNP  
**FIG. 34B**



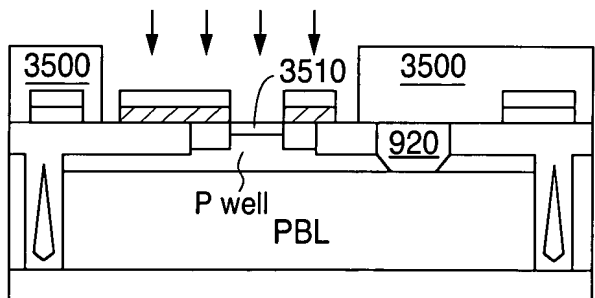
NMOS  
**FIG. 34C**



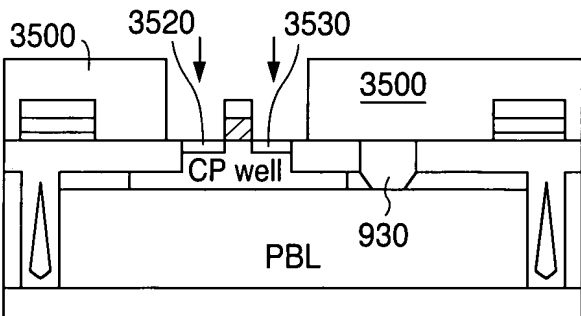
PMOS  
**FIG. 34D**



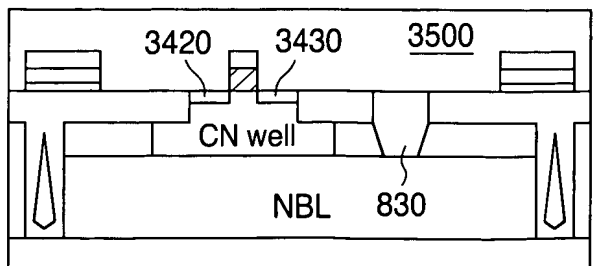
NPN  
**FIG. 35A**



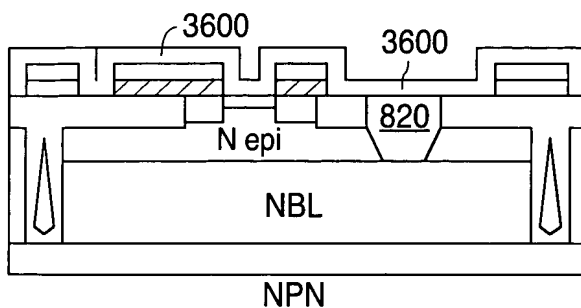
PNP  
**FIG. 35B**



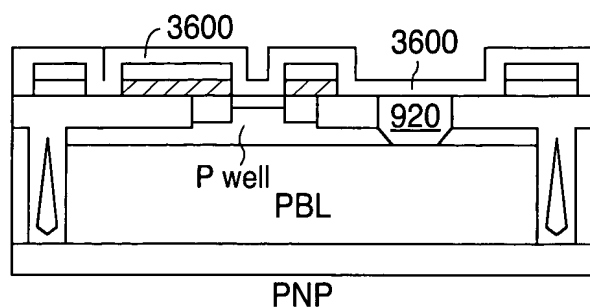
NMOS  
**FIG. 35C**



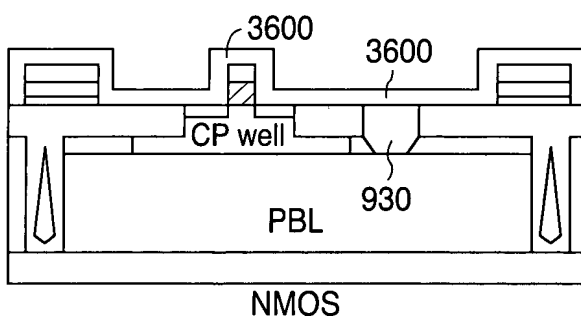
PMOS  
**FIG. 35D**



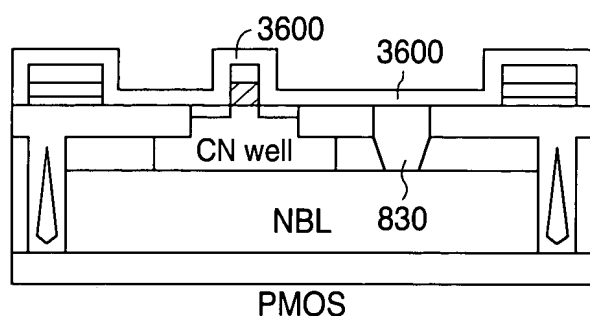
NPN  
**FIG. 36A**



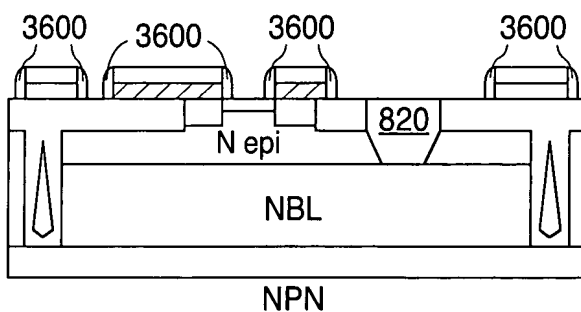
PNP  
**FIG. 36B**



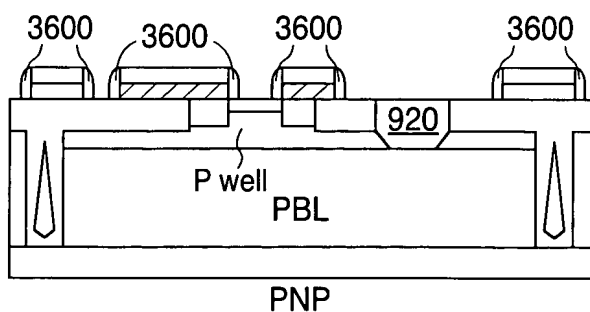
NMOS  
**FIG. 36C**



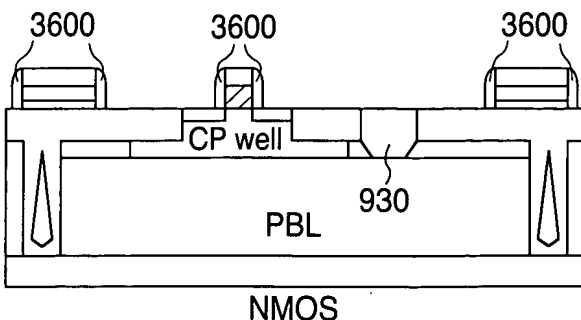
PMOS  
**FIG. 36D**



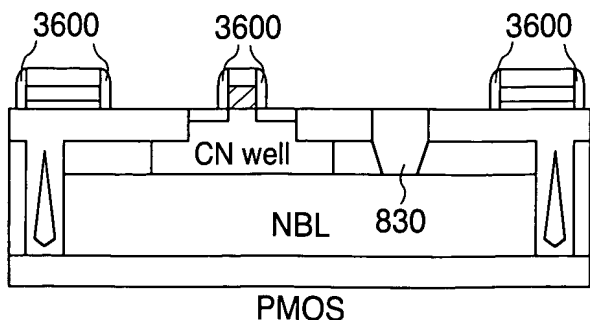
NPN  
**FIG. 37A**



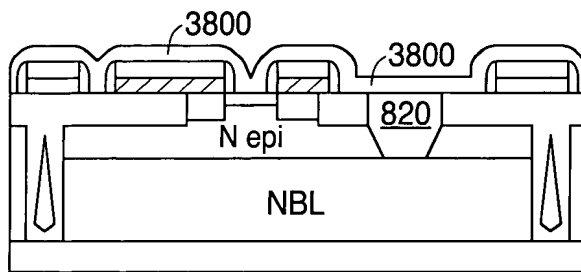
PNP  
**FIG. 37B**



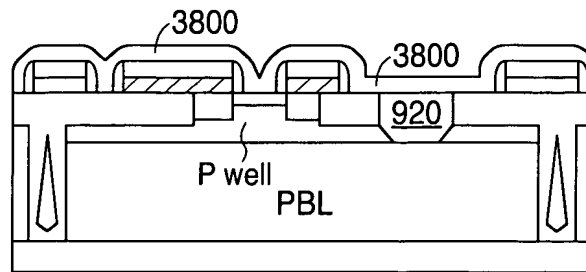
NMOS  
**FIG. 37C**



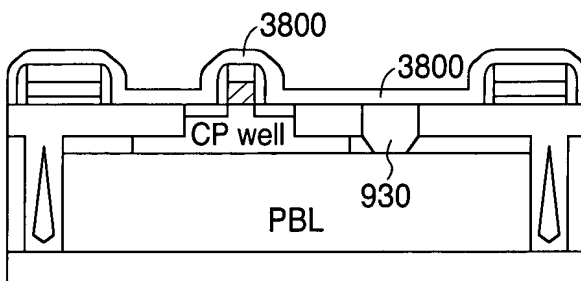
PMOS  
**FIG. 37D**



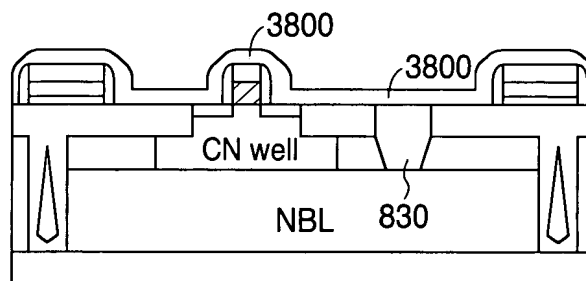
NPN  
**FIG. 38A**



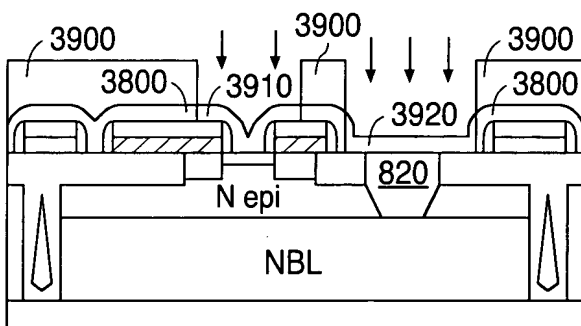
PNP  
**FIG. 38B**



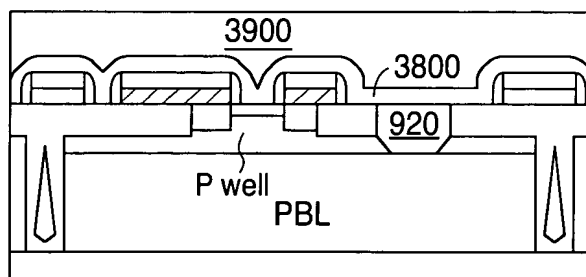
NMOS  
**FIG. 38C**



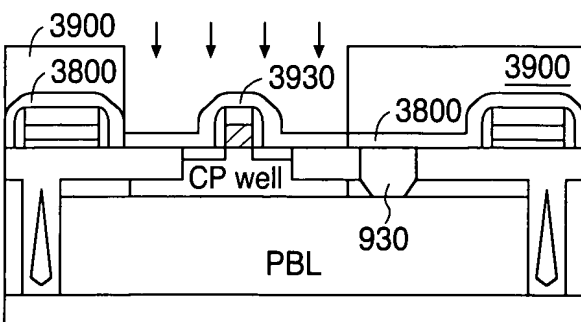
PMOS  
**FIG. 38D**



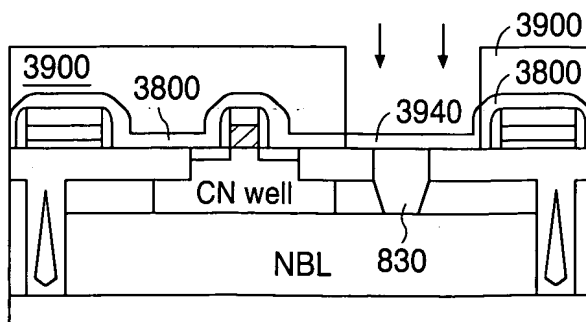
NPN  
**FIG. 39A**



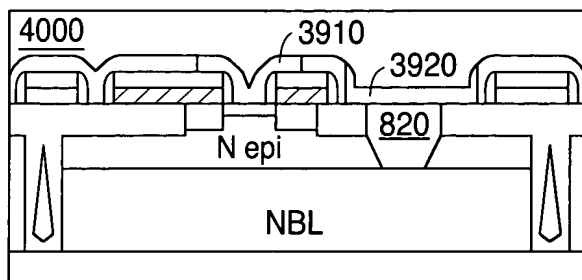
PNP  
**FIG. 39B**



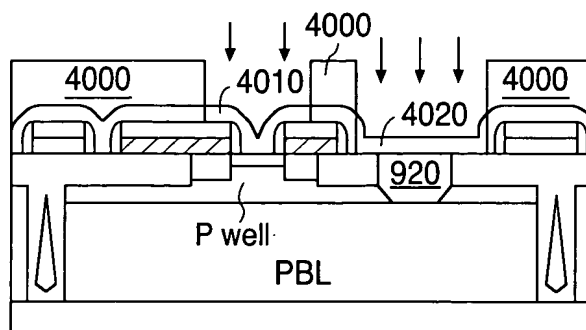
NMOS  
**FIG. 39C**



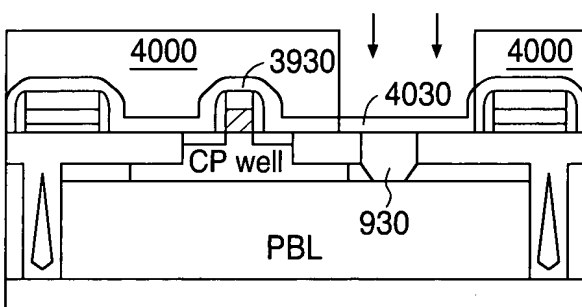
PMOS  
**FIG. 39D**



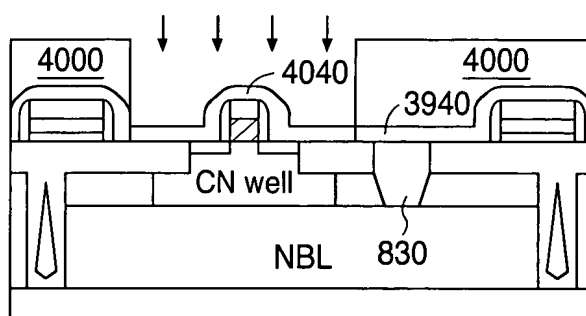
NPN  
**FIG. 40A**



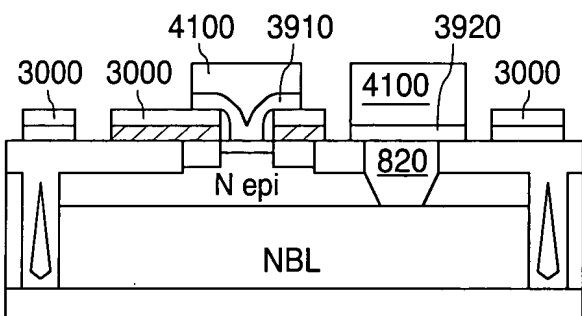
PNP  
**FIG. 40B**



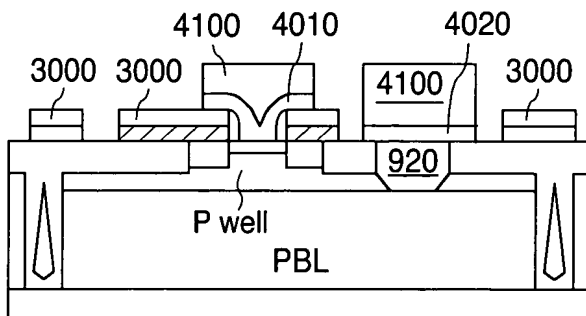
NMOS  
**FIG. 40C**



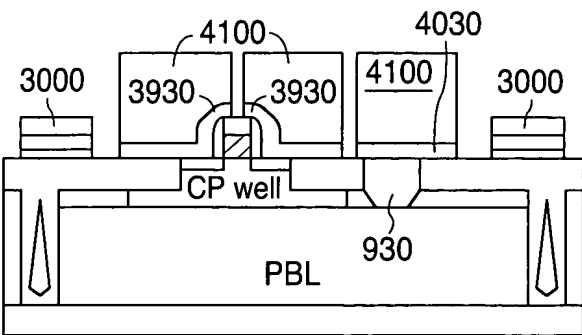
PMOS  
**FIG. 40D**



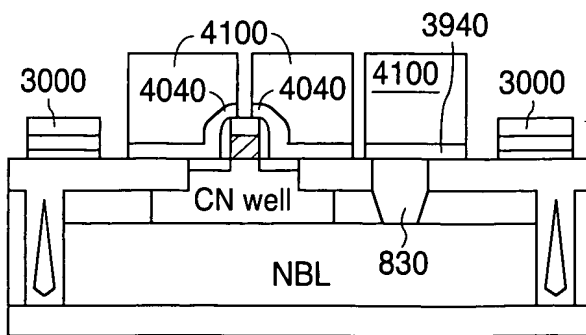
NPN  
**FIG. 41A**



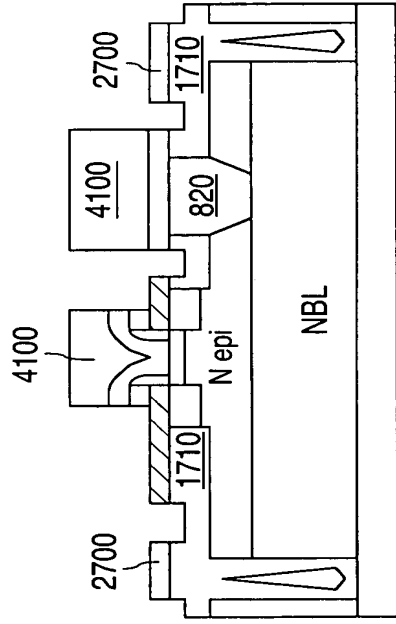
PNP  
**FIG. 41B**



NMOS  
**FIG. 41C**

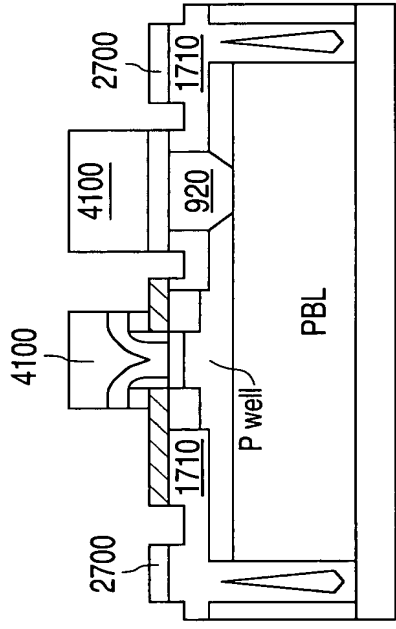


PMOS  
**FIG. 41D**



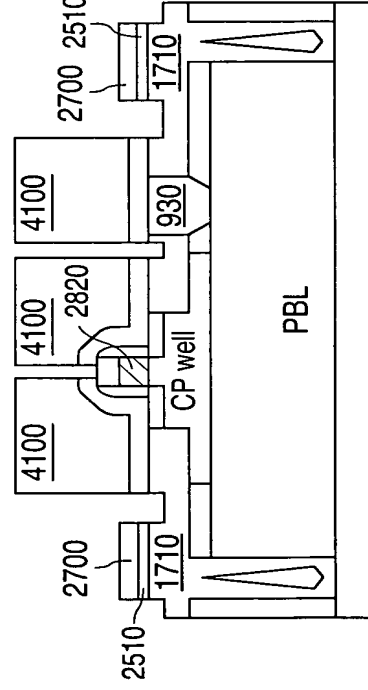
NPN

**FIG. 42A**



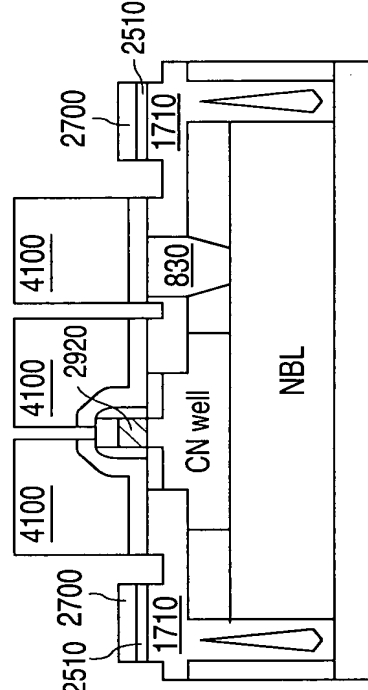
PNP

**FIG. 42B**



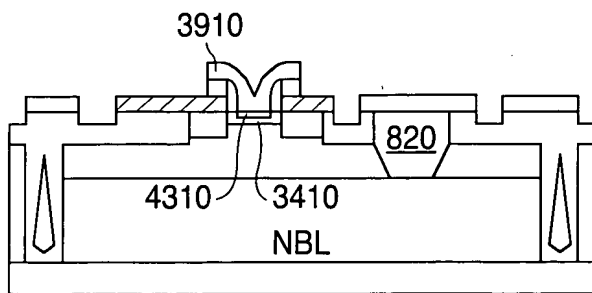
NMOS

**FIG. 42C**



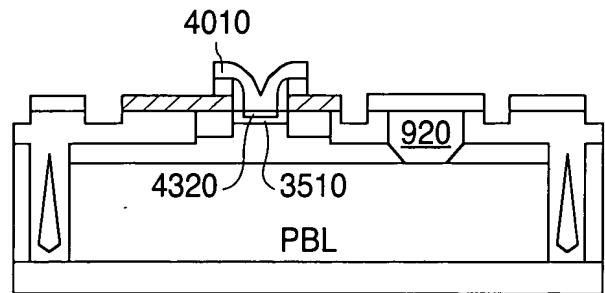
PMOS

**FIG. 42D**



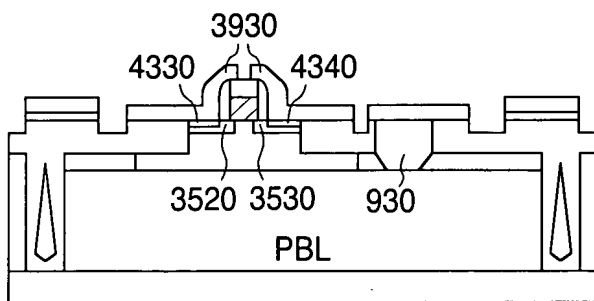
NPN

**FIG. 43A**



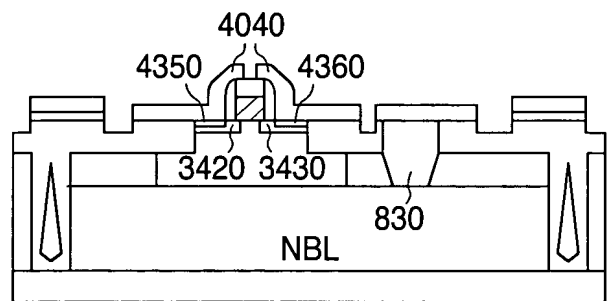
PNP

**FIG. 43B**



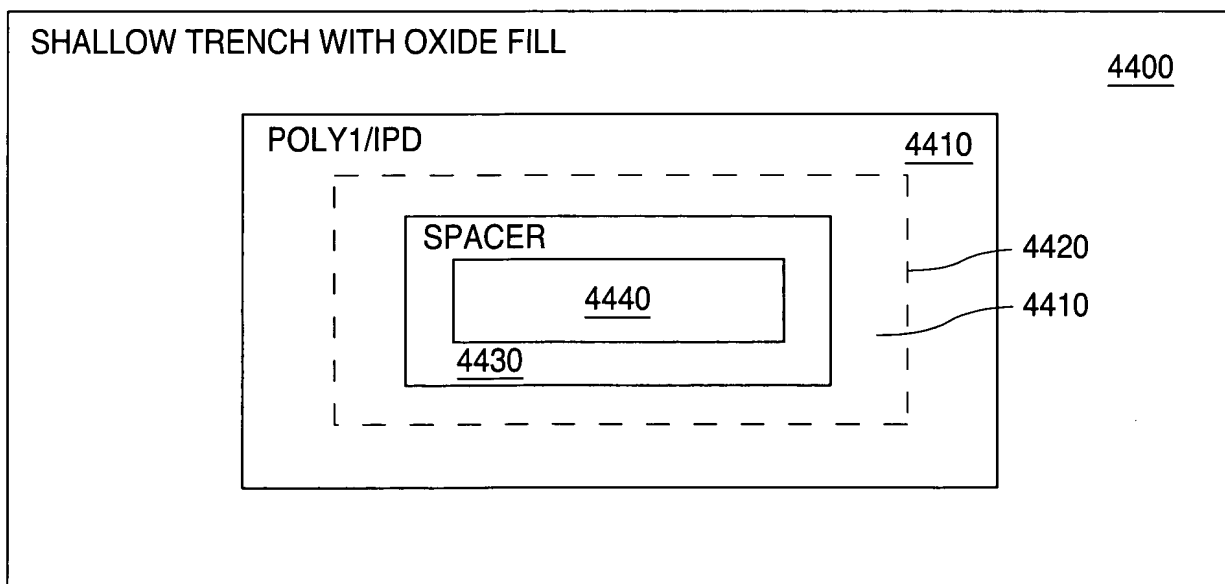
NMOS

**FIG. 43C**



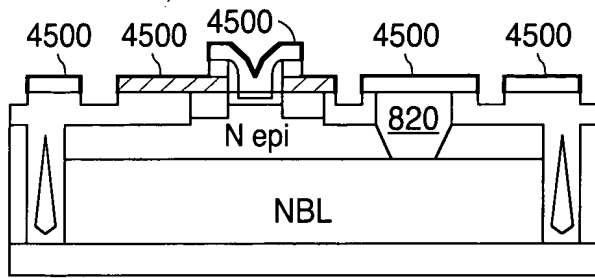
PMOS

**FIG. 43D**

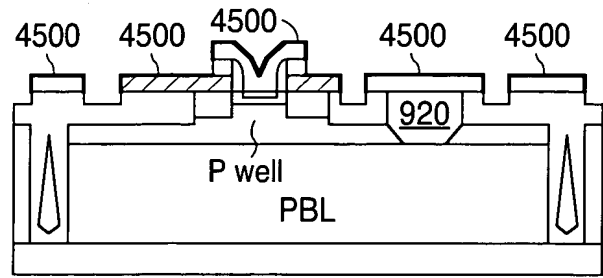


**FIG. 44**

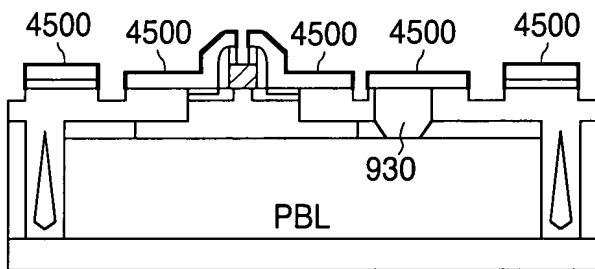




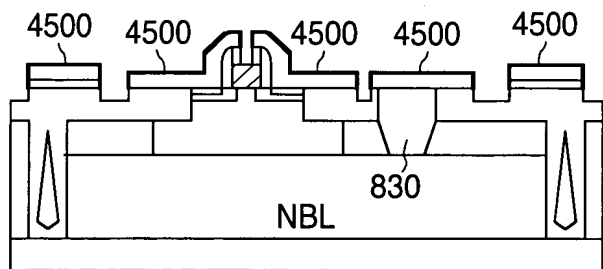
NPN  
**FIG. 45A**



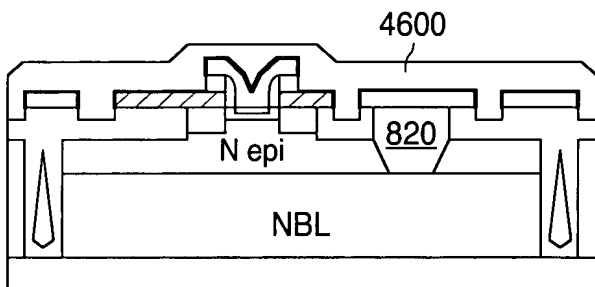
PNP  
**FIG. 45B**



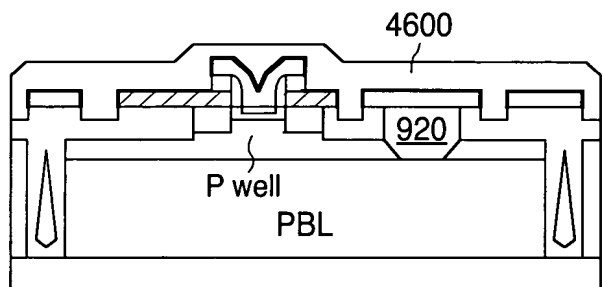
NMOS  
**FIG. 45C**



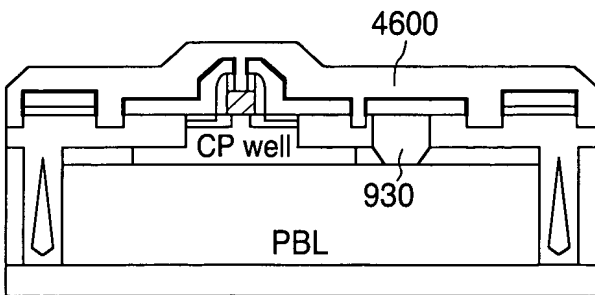
PMOS  
**FIG. 45D**



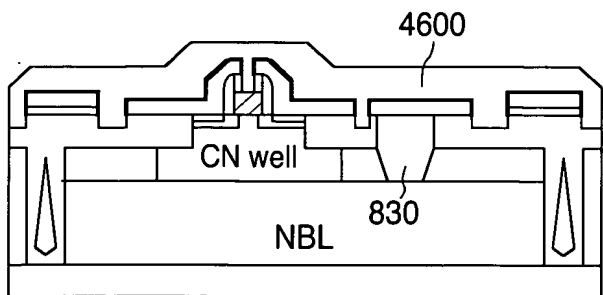
NPN  
**FIG. 46A**



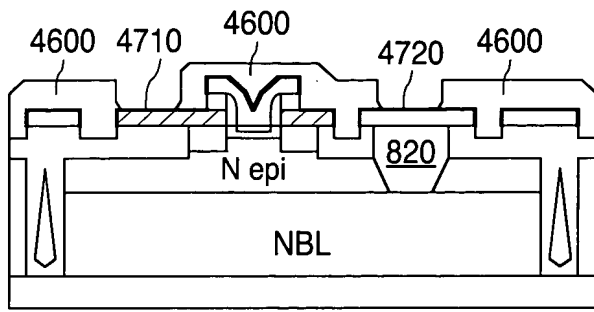
PNP  
**FIG. 46B**



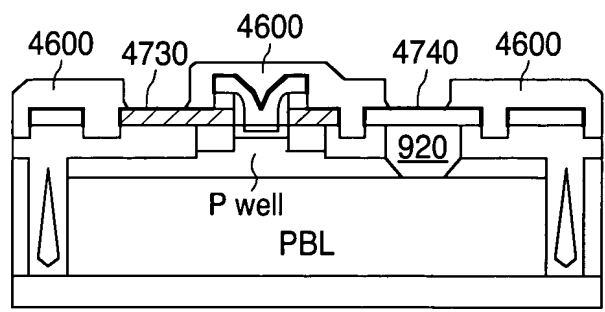
NMOS  
**FIG. 46C**



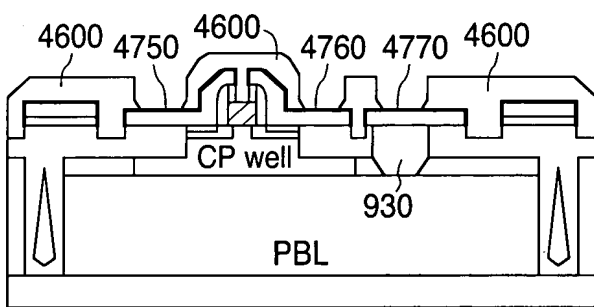
PMOS  
**FIG. 46D**



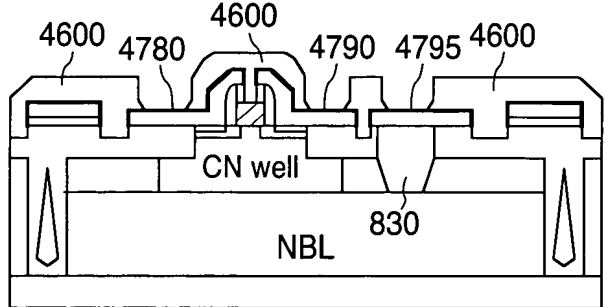
NPN  
**FIG. 47A**



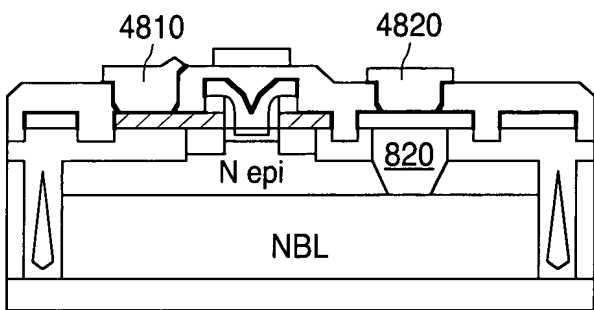
PNP  
**FIG. 47B**



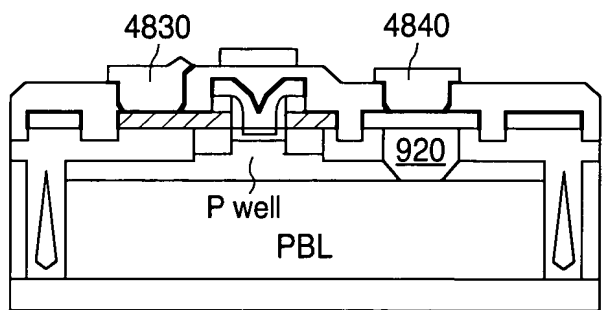
NMOS  
**FIG. 47C**



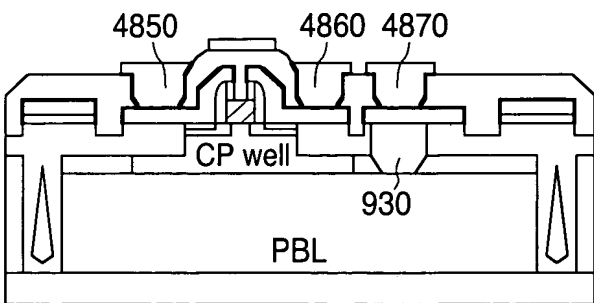
PMOS  
**FIG. 47D**



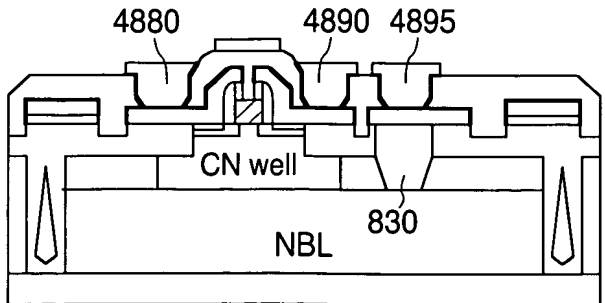
NPN  
**FIG. 48A**



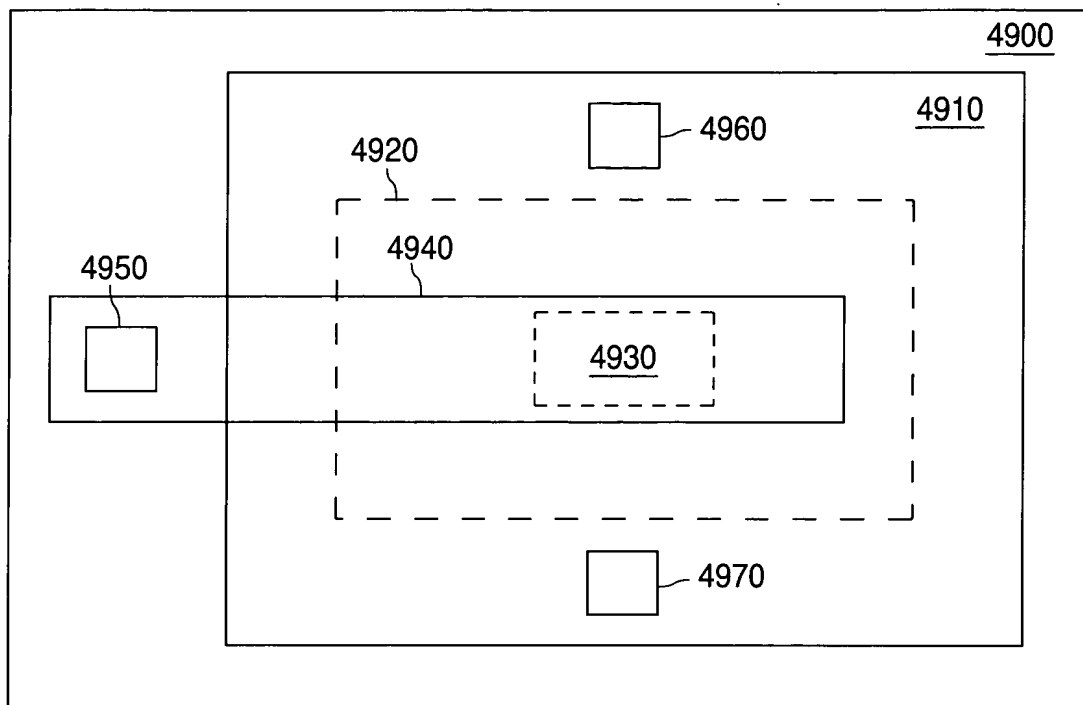
PNP  
**FIG. 48B**



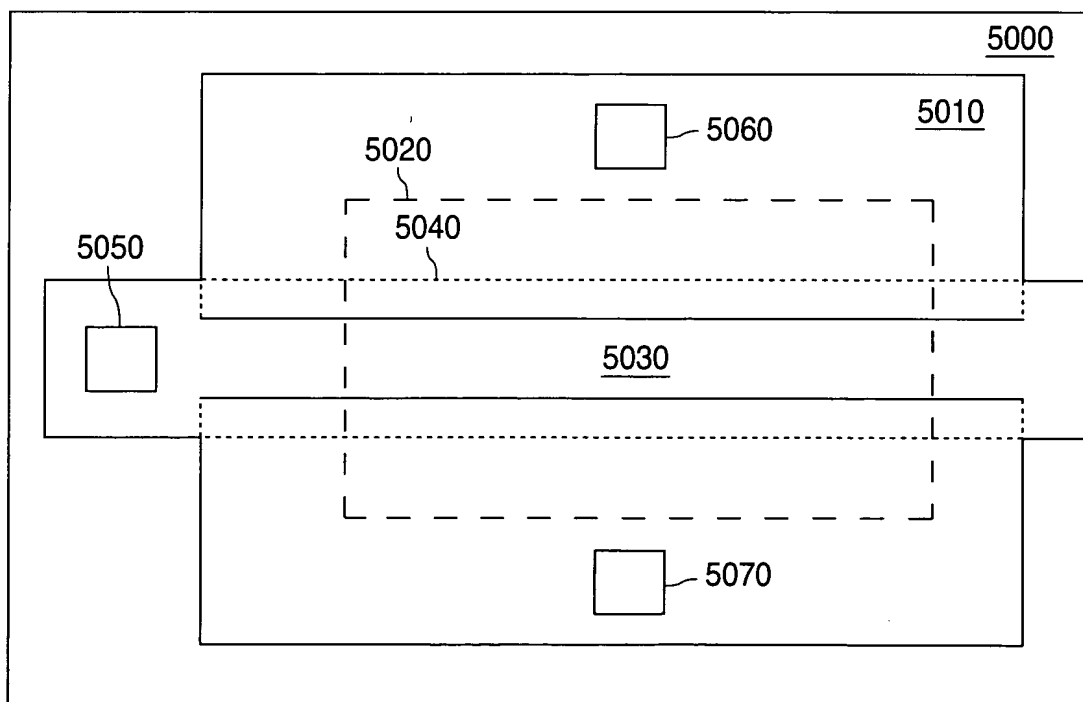
NMOS  
**FIG. 48C**



PMOS  
**FIG. 48D**



**FIG. 49**



**FIG. 50**